

Properties of Plain, Pure, and Safe Petri Nets

– with some applications to Petri net synthesis –

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Abstract. A set of necessary conditions for a Petri net to be plain, pure and safe is derived. It is argued that these conditions are applicable, both in practice (in the interest of Petri net synthesis), and in theory (e.g., as part of a characterisation of the reachability graphs of live and safe marked graphs).

Keywords: Labelled transition systems, marked graphs, Petri nets.

1 Introduction

In the first part (section 2) of this paper, we examine a number of properties which are typical for plain, pure and safe (pps) Petri nets. Such nets are intimately related to elementary Petri net systems [7–10]. We shall also argue that these properties can be employed usefully in the quick-fail, pre-synthesis stage of a Petri net synthesis algorithm, if it is aimed at pps nets.

In the second part (section 3) of the paper, we apply our results in order to obtain a characterisation of the state spaces of live and safe marked graphs [5]. This result will be built upon the characterisation of live and bounded marked graphs previously described in [3].

Basic definitions about transition systems and Petri nets have been transferred to the appendix (section A), in order to make the paper self-contained, without impeding its readability for readers familiar with the basic theory. In conformance with the book [1] on net synthesis, we shall consider arc-labelled transition systems and transition-unlabelled Petri nets.

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2 Some necessary conditions for pps Petri nets

In the following, we shall use letters $a, b, c, \dots \in T$ (also $t \in T$) for letters of a transition system (or, respectively, for the corresponding transitions of a Petri net); $u, v, w \in T^*$ for sequences of transitions; p, q for the places of a net; M, K, L for the markings of a net; and r, s for the states of an lts. The \bullet and \blacklozenge notation, which is much used in the following, is explained in section A (definition 7).

2.1 Properties of plain, pure, and safe Petri nets

Proposition 1. PROPERTIES OF PPS NETS

Let $N = (P, T, F, M_0)$ be a pps net, let a, b, c be (not necessarily different) transitions in T , let M, M', K, \dots be reachable markings, and let w, v, u, \dots be sequences of transitions. Then

- (A) If $M[wv]$ or $M[vw]$, then $\blacklozenge w \cap \blacklozenge v = \emptyset = w^\blacklozenge \cap v^\blacklozenge$.
- (B) If $M'[a]M$ and $M''[b]M$, then $[b]M' \iff [a]M''$.
- (C) If $M[a]$ and $M[b]$, then $a^\bullet \cap b^\bullet = \emptyset = \bullet a \cap \bullet b$.
- (D) If $M[a]$ and $M[b]$, then for any K : $(K[ab] \iff K[ba])$.
- (E) If $M[awb]$ then $(\bullet a \cap \bullet b) \subseteq w^\blacklozenge$ and $(a^\bullet \cap b^\bullet) \subseteq \blacklozenge w$.
- (F) If $M[wv]$ and $M[vw]$ and $M[wc]$ and $M[vc]$, then $M[wc]M'$ and $M[vwc]M'$ and $M[c]$.
- (G) - If $(M_1[x_1w_1y_1])$ and $(M_2[x_2w_2y_2])$ with $x_1, y_1, x_2, y_2 \in \{a, b\}$, and $(\bullet a \cap \bullet b \neq \emptyset$ or $a^\bullet \cap b^\bullet \neq \emptyset)$, and there exist v_1, v_2, v_3 such that $\Psi(w_1) = \Psi(v_1) + \Psi(v_2)$, $\Psi(w_2) = \Psi(v_2) + \Psi(v_3)$, $\Psi(w_3) = \Psi(v_3) + \Psi(v_1)$,
- then $\neg K[x_3w_3y_3]$ for $x_3, y_3 \in \{a, b\}$.

Proof:

(A): Suppose $M[w]M_1[v]M_2$. If $\Psi(w) = \Psi(v)$ then if $p \in \blacklozenge w$ and $p \in \blacklozenge v$ then $M(p) = M_2(p) + 2$ as well as if $p \in w^\blacklozenge$ and $p \in v^\blacklozenge$ then $M_2(p) = M(p) + 2$. Both situations contradicts safeness. This implies that $\blacklozenge w = \emptyset = w^\blacklozenge$ and $\blacklozenge v = \emptyset = v^\blacklozenge$ and the claim is true. Assume now that $\Psi(w) \neq \Psi(v)$. If $p \in \blacklozenge w \cap \blacklozenge v$ then $M(p) = 1$, $M_1(p) = 0$ and $M_2(p) = -1$. Contradiction. Similarly, if $p \in w^\blacklozenge \cap v^\blacklozenge$ then $M(p) = 0$, $M_1(p) = 1$ and $M_2(p) = 2$. Contradiction. Hence $\blacklozenge w \cap \blacklozenge v = \emptyset = w^\blacklozenge \cap v^\blacklozenge$, as was claimed.

(B): Suppose that $M'[a]M$ and $M''[b]M$ and $K[b]M'$, for some reachable K , and $\neg([a]M'')$. By $\neg([a]M'')$, there is some place $p \in a^\bullet \setminus \bullet a$ with $M''(p) = 0$. By $M'[a]M$, both $M'(p) = 0$ and $M(p) = 1$. By $M''[b]M$, $p \in b^\bullet \setminus \bullet b$. By $K[b]M'$, $M'(p) = 1$, contradicting $M'(p) = 0$. Hence $K'[a]M''$ for some reachable K' , and $K' = K$ because K and K' can be backward-reached from M by the same Parikh vector.

(C,D): If $a = b$, the claim follows by the absence of side-places.

Suppose that $a \neq b$ and $M[a]$ and $M[b]$. If $p \in a^\bullet \cap \bullet b$, then $M(p) = 0$ since M enables a , but also $M(p) = 1$ since M enables b , which is a contradiction. If

$p \in \bullet a \cap b \bullet$, we get a symmetrical contradiction. To show the remaining part of the claim, suppose that $K[ab]$. Using (A) applied to K with $w = a$ and $v = b$ and knowing that $\bullet a = \blacklozenge a$ and $a \bullet = a \blacklozenge$ we get $(\bullet a \cup a \bullet) \cap (\bullet b \cup b \bullet) = \emptyset$ (i.e., intuitively, a and b are completely independent), and thus also $K[ba]$. The reverse direction is symmetrical.

(E): Suppose that $M[a]M_1[w]M_2[b]M'$ and that $p \in (\bullet a \cap \bullet b)$. Then $M(p) = 1$, $M_1(p) = 0$, $M_2(p) = 1$, and $M'(p) = 0$, so that w acts positively on p , that is, $p \in w \blacklozenge$. Similarly for $p \in (a \bullet \cap b \bullet)$: $M(p) = 0$, $M_1(p) = 1$, $M_2(p) = 0$ and $M'(p) = 1$ w acts negatively on p , that is, $p \in \blacklozenge w$.

(F): Suppose that $M[w]M_1[v]M_2$ and $M[v]M_3[w]M_4$. Of course from the firing rule we know that $M_3 = M_4$. By (A) and $M[w]M_1[c]$ with $M[v]M_3[c]$ we get: $\bullet c \cap \blacklozenge v = \emptyset = \bullet c \cap \blacklozenge w$ and $c \bullet \cap v \bullet = \emptyset = c \bullet \cap w \bullet$.

From $M_1[c]$ we know that for all $p \in \bullet c$ we have $M_1(p) = 1$ and from the previous considerations $p \notin \blacklozenge v$. Assume now that $p \in v \bullet$. Then $M_2(p) = 2$. Contradiction. Hence $\bullet c \cap (\blacklozenge v \cup v \bullet) = \emptyset$.

From $M_3[c]$ we know that for all $p \in \bullet c$ we have $M_3(p) = 1$ and from the previous considerations $p \notin \blacklozenge w$. Assume now that $p \in w \bullet$. Then $M_2(p) = 2$. Contradiction. Hence $\bullet c \cap (\blacklozenge w \cup w \bullet) = \emptyset$.

From the other hand, from $M_1[c]$ we know that for all $p \in c \bullet$ we have $M_1(p) = 0$ and from the previous considerations $p \notin v \bullet$. Assume now that $p \in \blacklozenge v$. Then $M_2(p) = -1$. Contradiction. Hence $c \bullet \cap (\blacklozenge v \cup v \bullet) = \emptyset$.

From $M_1[c]$ we know that for all $p \in c \bullet$ we have $M_3(p) = 0$ and from the previous considerations $p \notin w \bullet$. Assume now that $p \in \blacklozenge w$. Then $M_2(p) = -1$. Contradiction. Hence $c \bullet \cap (\blacklozenge w \cup w \bullet) = \emptyset$.

Together we have $(\bullet c \cup c \bullet) \cap (\blacklozenge v \cup v \bullet) = \emptyset$ as well as $(\bullet c \cup c \bullet) \cap (\blacklozenge w \cup w \bullet) = \emptyset$ and it is straightforward that $M[c]$ and $M_2[c]$.

(G): Assume $M_1[x_1]M_{1.1}[w_1]M_{1.2}[y_1]M'_1$, $M_2[x_2]M_{2.1}[w_2]M_{2.2}[y_2]M'_2$ and

$M_3[x_3]M_{3.1}[w_3]M_{3.2}[y_3]M'_3$ with $x_1, y_1, x_2, y_2, x_3, y_3 \in \{a, b\}$ as well as there exist v_1, v_2, v_3 such that $\Psi(w_1) = \Psi(v_1) + \Psi(v_2)$, $\Psi(w_2) = \Psi(v_2) + \Psi(v_3)$, and $\Psi(w_3) = \Psi(v_3) + \Psi(v_1)$.

Case 1: $\bullet a \cap \bullet b \neq \emptyset$

Since $M_1[x_1]M_{1.1}[w_1]M_{1.2}[y_1]M'_1$, for every place $p \in \bullet a \cap \bullet b$ we have $M_1(p) = 1$, $M_{1.1}(p) = 0$, $M_{1.2}(p) = 1$, hence $ef_p(w_1) = 1$. In a similar way we can observe that $ef_p(w_2) = 1$ and $ef_p(w_3) = 1$. Of course $1 = ef_p(w_2) = ef_p(v_1v_2) = ef_p(v_1) + ef_p(v_2)$ hence either (1) $ef_p(v_1) = 1$ and $ef_p(v_2) = 0$ or (2) $ef_p(v_1) = 0$ and $ef_p(v_2) = 1$. Assuming (1) we obtain $1 = ef_p(w_2) = ef_p(v_2v_3) = ef_p(v_2) + ef_p(v_3) = 0 + ef_p(v_3)$, hence $ef_p(v_3) = 1$. But then $ef_p(w_3) = ef_p(v_3v_1) = ef_p(v_3) + ef_p(v_1) = 1 + 1 = 2$, which contradicts $ef_p(w_3) = 1$. Assuming (2) we obtain $1 = ef_p(w_2) = ef_p(v_2v_3) = ef_p(v_2) + ef_p(v_3) = 1 + ef_p(v_3)$, hence $ef_p(v_3) = 0$. But then $ef_p(w_3) = ef_p(v_3v_1) = ef_p(v_3) + ef_p(v_1) = 0 + 0 = 0$, which contradicts $ef_p(w_3) = 1$.

Case 2: $a^\bullet \cap b^\bullet \neq \emptyset$

Since $M_1[x_1]M_{1.1}[w_1]M_{1.2}[y_1]M'_1$, for every place $p \in a^\bullet \cap b^\bullet$ we have $M_1(p) = 0$, $M_{1.1}(p) = 1$, $M_{1.2}(p) = 0$, hence $ef_p(w_1) = -1$. Similarly, $ef_p(w_2) = -1$ and $ef_p(w_3) = -1$. Of course $-1 = ef_p(w_1) = ef_p(v_1v_2) = ef_p(v_1) + ef_p(v_2)$ hence either (1) $ef_p(v_1) = -1$ and $ef_p(v_2) = 0$ or (2) $ef_p(v_1) = 0$ and $ef_p(v_2) = -1$. Assuming (1) we obtain $-1 = ef_p(w_2) = ef_p(v_2v_3) = ef_p(v_2) + ef_p(v_3) = 0 + ef_p(v_3)$, hence $ef_p(v_3) = -1$. But then $ef_p(w_3) = ef_p(v_3v_1) = ef_p(v_3) + ef_p(v_1) = -1 + (-1) = -2$, which contradicts $ef_p(w_3) = -1$. Assuming (2) we obtain $-1 = ef_p(w_2) = ef_p(v_2v_3) = ef_p(v_2) + ef_p(v_3) = -1 + ef_p(v_3)$, hence $ef_p(v_3) = 0$. But then $ef_p(w_3) = ef_p(v_3v_1) = ef_p(v_3) + ef_p(v_1) = 0 + 0 = 0$, which contradicts $ef_p(w_3) = -1$.

This finishes the proof. \square

2.2 Some discussion, and some examples

Proposition 1 implies that in any pps net reachability graph, any pair of two distinct transitions (a, b) can be classified structurally into two types:

- $a^\bullet \cap b^\bullet = \emptyset$. Suppose that some marking M enables both a and b . Then, as a consequence of part (C) or (D) of proposition 1 and safeness, also $a^\bullet \cap b^\bullet = \emptyset$. Therefore, $M[a] \wedge M[b]$ implies the existence of a full diamond $M[ab] \wedge M[ba]$ in the reachability graph. Moreover, if one such full diamond exists for a and b , any other branching points $K[a] \wedge K[b]$ can also be extended to full diamonds $K[ab] \wedge K[ba]$.
- $a^\bullet \cap b^\bullet \neq \emptyset$. Then any marking M enabling both a and b only leads to a proper triangle, i.e. $\neg M[ab] \wedge \neg M[ba]$. In particular, there can be no “half-persistence” [2] such as $\neg M[ab] \wedge M[ba]$ or $M[ab] \wedge \neg M[ba]$.

The two relations are symmetric but not, in general, transitive. Moreover, there is no backward version of Proposition 1(C) or (D). Therefore, the above classification cannot be applied fully for postsets instead of presets of transitions. Indeed, Figure 1 shows a reachability graph which has a full diamond and, at the same time, a state-disjoint proper backward triangle between transitions a and b .

Moreover, two transitions may have a common pre-place never being enabled at the same time. See figure 2 for an example.

The properties (A)–(G) of proposition 1 come in two varieties:

- **Non-structural properties.** This name applies to properties (B), (D), and (F), since they do not contain any reference to a generating Petri net. Therefore, (B), (D) and (F) can be taken as properties of any arbitrary transition system.
- **Structural properties.** This applies to properties (A), (C), (E), and (G), since they make partial reference to a generating Petri net, by mentioning the pre- and/or postsets of transitions and/or transition sequences.

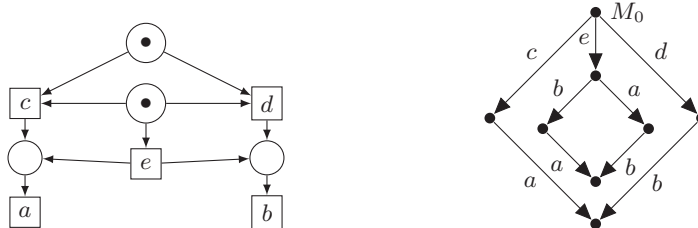


Fig. 1. A pps Petri net whose reachability graph displays a diamond and a proper backward triangle.

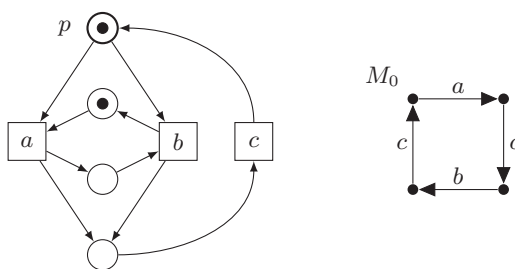


Fig. 2. A pps Petri net in which transitions a and b share the pre-place p but are not enabled simultaneously at any reachable marking (l.h.s.) and its reachability graph (r.h.s.).

Mathematically speaking, (B), (D), and (F) are redundant, since for pps nets,

$$(A) \wedge (C) \Rightarrow (B) \wedge (D) \wedge (F)$$

(The proofs of these claims are easy and will be omitted in the present paper.) Speaking in terms of applications, however, the non-structural conditions can be more useful than the structural ones. If a transition system is given without any generating Petri net, then (B), (D) and (F) can be tested straight away, while it can be very difficult to test one of the other properties when we also need to consider the possible shapes of any (hoped-for) Petri net solutions. For this reason, the non-structural properties can be used as limiting conditions on the class of transition systems eligible for Petri net (pre-)synthesis; this will be discussed later, in section 2.3. We finally point out that, in general, (B), (D) and (F) are independent of each other. This is proved by the labelled transition systems depicted in figure 3. Of course, due to the previous proposition, none of the three lts is actually pps solvable. (But all of them have a Petri net solution. APT [11] is useful in confirming this.)

We now briefly discuss the relevance, and the independence, of property (G).

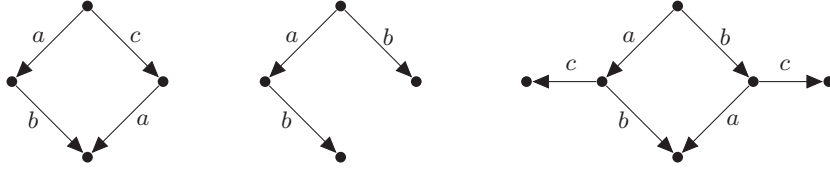


Fig. 3. Lts satisfying, respectively, $\neg(B)\wedge(D)\wedge(F)$, $(B)\wedge\neg(D)\wedge(F)$, $(B)\wedge(D)\wedge\neg(F)$.

- Consider the labelled transition system TS_1 (only solid edges) depicted in figure 4. From the pattern around state K , more precisely, from $K[a]$ and $K[b]$ and neither $K[ab]$ nor $K[ba]$, it follows that in any pps solution of TS_1 , there must exist a place $p \in (\bullet a \cap \bullet b)$. But then, (G) is violated, letting $v_1 = d_1d_1$, $v_2 = e_1e_1$, and $v_3 = c_1c_2$. Nevertheless, TS_1 satisfies the properties (B), (D) and (F) (and, we believe, also (A), (C) and (E), for any pps solution). In this sense, (G) “explains” the non-pps-solvability of TS_1 .
- Now consider the transition system TS_2 (solid and dashed edges) shown in the same figure. The *raison d’être* for a place such as p above has disappeared, and TS_2 satisfies, in fact, (G), along with (B), (D) and (F). Thus, none of these properties can be used in order to explain its non-pps-solvability.

So far, we know of no general property which explains the non-pps-solvability of TS_2 in a similar way as (G) explains the non-pps-solvability of TS_1 . It should come as no surprise at all that such examples can be found: obtaining an exact characterisation of the state spaces of pps Petri nets seems to be quite out of reach, at the present time.

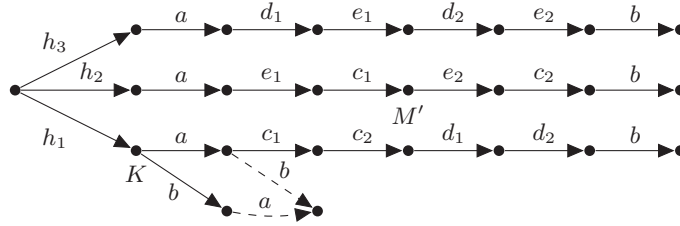


Fig. 4. Two lts’s, TS_1 (containing only solid edges), and TS_2 (containing solid as well as dashed edges). Both TS_1 and TS_2 are non-pps-solvable. TS_1 violates (G), and TS_2 satisfies all of (A)–(G).

2.3 Pre-synthesis, and other non-structural conditions

Proposition 1 yields an upper approximation of the class of pps solvable lts. The transition system TS_2 shown in figure 4 proves that this is a true approximation, although its relative complexity suggests that it may be a reasonably good one, for the time being.

Consequently, the non-structural properties (B), (D), (F) described in proposition 1 can be useful for synthesis. Suppose that a labelled transition system TS is given and a pps solution is sought for it. Before actual synthesis begins, i.e. in a pre-synthesis stage, it is possible to check (B), (D) and (F). If one of them fails, then pps synthesis is infeasible; such an effect is usually called *quick fail*. In a quick-fail case, we might reject TS immediately, with two benefits: first, a costly synthesis does not need to be started, and secondly, a meaningful – possibly very useful – error message can be issued.

Quick-fail pre-synthesis may be relatively cheap (as when checking (B) or (D)), or relatively costly (as when checking (F), for instance). Other properties may or may not be checked easily, depending on the circumstances. Therefore, we shall next give a list (in no particular order and without full proof) of non-structural properties which can be used for quick-fail purposes. All of them, except the last one, are consequences of properties (B), (D) and (F) described in proposition 1. Some of them may be easier to check than (F). The last property mentioned in proposition 2 actually follows from proposition 1(G).

Proposition 2. MORE NON-STRUCTURAL PROPERTIES OF PPS NETS

Let $N = (P, T, F, M_0)$ be a pps net, let a, b, c be (not necessarily different) transitions in T , let M, M', K, \dots be reachable markings, and let w, v, u, \dots be sequences of transitions. Then the following properties are true:

- If $M[w]M'[v]M''$ for $w, v \in T^*$ such that $\Psi(w) = \Psi(v)$, then $M = M' = M''$.
- If $M[ab]$ and $M[ba]$ and $M[ac]$ and $M[bc]$, then $M[c]$.
- If $M[a]$ and $M[b]$ and $K[ab]$, then $M[ab]$ and $M[ba]$ and $K[ab]$ and $K[ba]$.
- If $M[a]M'$ and $M[b]M''$, then $M'[b] \iff M''[a]$.
- If $M[a]M'$, $M[b]M''$, $\neg M[ab]$, and $\neg M[ba]$, then neither $K[ab]$ nor $K[ba]$.
- If $M[bu_1]$ and $M[u_2]$ and $\Psi(u_1) = \Psi(u_2)$ then $M[u_2b]$.
- If $M[bwa]$ and $M[av]$ and $\Psi(w) = \Psi(v)$, then $M[avb]$.
- If $M[wvw]$ and $K[wv']$ and $\Psi(v) = \Psi(v')$, then $K[wv'w]$.
- If $M[wvw']$ and $K[wv']$ and $\Psi(v) = \Psi(v')$ and w' is a prefix of w , then $K[wv'w']$.
- If $M[tvt]$, $K[v']K'$ and $\Psi(v) = \Psi(v')$, then $K'[t]$.
- If $M[vwv]$, then for all reachable markings K such that $K[vw]K'$, we have $K'[v]$.
- If $M[awa]$, then for all reachable markings K such that $K[w]K'$, we have $K'[a]$.
- If $M[a]$ and $M[b]$ and $\neg M[ab]$, then neither $K[ab]$ nor $K[ba]$.
- If $M[aua]$ and $M'[ava]$ then $\neg K[awa]$ for any w such that $\Psi(w) = \Psi(u) + \Psi(v)$.

If $M[a]M'[b]$, $\neg M[b]$ and $K[b]$ then $\neg K[a]$.

If $\neg K[y]$ and $K[x]K_0[a_1 \dots a_m]K_m$ with $\Psi(a_1 \dots a_m)(y) = 0$, and if $K_j[y]$ for all $0 \leq j \leq m$, then $\neg K_m[x]$.

If $M[a]$, $M[b]$, $\neg M[ab]$ and $M'[x' u y']$, $M''[x'' v y'']$ for $x', x'', y', y'' \in \{a, b\}$ then $\neg K[x w y]$ for $x, y \in \{a, b\}$ and $\Psi(w) = \Psi(u) + \Psi(v)$. \square 2

3 The reachability graphs of safe marked graphs

In this section, we present a novel characterisation of the reachability graphs of safe, connected and live marked graphs, using the pps property (D) derived in the previous section. We first recall some of the results of [3]. Throughout the following (up to theorem 1), assume $TS = (S, \rightarrow, T, s_0)$ to be a transition system which is finite, totally reachable, deterministic, persistent, and backward persistent, reversible, and satisfies **P1**.

3.1 Distances, lattices, and marked graph synthesis

Definition 1. SHORT PATHS - DEFINITION 24 IN [3]

Let r, s be two states of an lts. A path $r[\tau]s$ will be called *short* if $|\tau| \leq |\tau'|$ for every path $r[\tau']s$, where $|\tau|$ denotes the length of τ . \square 1

Lemma 1. LEMMATA 25, 26, 27, AND 28 OF [3]

- In TS , a path $s[\tau]s'$ is short iff, for some $x \in T$, $\Psi(\tau)(x) = 0$.
- If $s[\tau]s'$ and $s[\tau']s'$ are both short, then $\Psi(\tau) = \Psi(\tau')$.
- Suppose that $s[\tau]s'$. Then $\Psi(\tau) = \Psi(\tau') + m \cdot \mathbf{1}$, for some number $m \in \mathbb{N}$, where $s[\tau']s'$ is any short path from s to s' .
- There is a short path between any pair of reachable markings. \square 1

By lemma 1, the following definition is sound.

Definition 2. DISTANCE - DEFINITION 29 OF [3]

Let s, s' be two states of TS , the Parikh vector of some short path from s to s' is called the *distance* between s and s' , and denoted by $\Delta_{s,s'}$. \square 2

For $x \in T$, define $\preceq_x \subseteq S \times S$ by $s \preceq_x s'$ iff $s[\alpha]s'$ for some $\alpha \in (T \setminus \{x\})^*$.

Proposition 3. LATTICES - LEMMA 30 AND 32, AND PROPOSITION 31 OF [3]

- $TS-x$ has state set S and label set $T \setminus \{x\}$; $(\preceq_x \setminus id)$ is acyclic, and the paths of $(TS-x, \preceq_x)$ are precisely the short paths of TS not containing x .
- For any label $x \in T$, $(TS-x, \preceq_x)$ is a complete lattice for the order \preceq_x .
- For every $x \in T$, there is a single state s_x enabling only x , and a single state r_x only produced by x , which are the top and bottom elements of $(TS-x, \preceq_x)$. \square 3

Lemma 2. LABELS ON SHORT PATHS INTO s_x - LEMMA 33 OF [3]

On any short path into s_x , there is no label x .

□ 2

Corollary 1. PROPERTIES OF $TS-x$ - COROLLARY 34 OF [3]

$(TS-x, \preceq_x)$ is acyclic, weakly connected, and a complete lattice with a unique maximal state s_x and a unique minimal state r_x .

□ 1

When a given lts TS is solved by an unlabelled Petri net [1], places should be introduced in order to constrain the behaviour, and more precisely, in order to prevent the occurrence of some transition x in those states of TS that do not enable x . It is therefore reasonable to consider the set of states that do not enable x but for which x is necessarily enabled after one further step, as follows.

Definition 3. SEQUENTIALISING STATES - DEFINITION 35 OF [3]

For any $x \in T$, $Seq(x) = \{s \in S \mid \neg s[x] \wedge (\forall a \in s^\bullet : s[ax])\}$.

□ 3

Theorem 1. MARKED GRAPH SYNTHESIS - THEOREM 40 IN [3]

*A labelled transition system TS is isomorphic to the reachability graph of a bounded, connected and live marked graph $MG(TS)$ iff it is totally reachable, deterministic, persistent, backward persistent, reversible, finite, and satisfies **P1**.*

□ 1

[3] also contains a characterisation of the bound, as follows.

Proposition 4. EXACT BOUNDS – LEMMA 42 IN [3]

The bound of the marked graph $MG(TS)$ is $\max\{\Delta_{s_y, s_x}(y) \mid y \in T, s_y \in Seq(x)\}$.

□ 4

3.2 The state spaces of live and safe marked graphs

Our aim is to replace the indirect characterisation of the bound given by proposition 4 by a direct one, using one of the properties derived in the previous section. The lts depicted in figure 5 illustrates this idea.

Let us first see why the condition of proposition 4 – for bound 1 – is violated. Consider the short path

$$6 = s_b [b \rangle 7 [e \rangle 2 [d \rangle 4 [b \rangle 9 [e \rangle s_a = 0$$

It contains b twice. Moreover, $s_b \in Seq(a)$. According to theorem 1 and proposition 4, any marked graph Petri net solution must necessarily have a non-safe

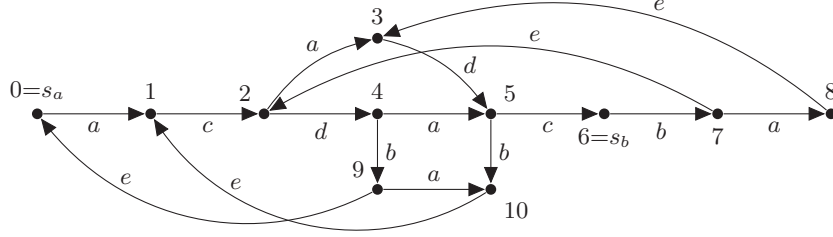


Fig. 5. An lts which has a 2-bounded but not a safe marked graph solution.

place with bound 2 leading from transition b to transition a . Note that there is also a short path

$$0 = s_a [a \rangle 1 [c \rangle 2 [d \rangle 4 [a \rangle 5 [c \rangle s_b = 6$$

containing a two times. However, this path is not indicative of an unsafe place from a to b , since $s_a \notin Seq(b)$.

We now show that, in order to characterise safeness, proposition 4 can essentially be replaced by condition (D), in the sense that the following two properties are equivalent for an lts TS :

1. TS is isomorphic to the reachability graph of a safe, connected and live marked graph;
2. TS is totally reachable, deterministic, persistent, backward persistent, reversible, finite, and satisfies properties **P1** and (D).

Since (1) \Rightarrow (2) is clear from the properties of live and safe marked graphs [5], and by part (D) of proposition 1, we only need to prove (2) \Rightarrow (1).

Lemma 3. (D) AND THE REST IMPLIES SAFE MARKED GRAPH SOLVABILITY

*Suppose that a transition system TS is totally reachable, deterministic, persistent, backward persistent, reversible, finite, and satisfies properties **P1** and (D). Then the maximum in proposition 4 does not exceed 1.*

Proof:

Consider a short path $s_y[y]s[\alpha]s_x$, with $s_y \in Seq(x)$, which contains another y in α . Consider the second y on such a path and an y -free prefix α' of α with $s_y[y]s[\alpha']s'[y]$. By lemma 2, α (and thus α') is x -free. Let $M = s'$ and $K = s_y$. Because $K \in Seq(x)$, we get $\neg K[x]$ as well as $K[yx]$. Along α' , transition x never gets disabled because it is not contained in α' , and because of persistence. Hence $M[x]$. Thus, we get $M[x]$ and $M[y]$ and $K[yx]$ and $\neg K[xy]$.

In other words, by contraposition, if the maximum in proposition 4 exceeds 1, then $\neg(\text{D})$ holds true. This proves the claim. \square 3

To illustrate this lemma, we show how (D) is violated in figure 5. Consider $x = a$, $y = b$, $K = 6$, and $M = 4$. We have $M[a]$ and $M[b]$, as well as $K[ba]$. According to (D), we should also have $K[ab]$, but contrariwise, $\neg 6[a]$ in figure 5.

Corollary 2. CHARACTERISATION OF SAFE MARKED GRAPHS

A labelled transition system is isomorphic to the reachability graph of a safe, connected and live marked graph iff it is totally reachable, deterministic, persistent, backward persistent, reversible, finite, and satisfies P1 as well as (D). \square 2

The difference to theorem 1 is that, in addition, safeness is characterised by (D).

4 Concluding Remarks

In the first part of this paper, we have derived a collection of properties of plain, pure and safe Petri nets. The ambition is that these properties encompass (or imply) many generally known properties of pps nets and – by proxy – elementary nets, causing the resulting upper approximation of pps state spaces to be as tight as possible. In the second part of the paper, we have shown how these properties can contribute to the characterisation of a class of pps solvable transition systems, more precisely, transition systems solvable by live and safe marked graphs.

Our hopes for future work are twofold. It would be nice if the properties listed here can be usefully integrated in synthesis or pre-synthesis tools, for instance in order to allow sophisticated quick-fail mechanisms. Also, it would be nice if they could help in obtaining direct characterisations of the state spaces of net classes which are different from marked graphs, such as live and pps free-choice nets, or reversible, pps, persistent nets (though the difficulty of such a task should not be underestimated).

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A Labelled transition systems and Petri nets

Definition 4. LTS, REVERSE LTS, REACHABILITY, PARIKH VECTORS, CYCLES

A labelled transition system with initial state, abbreviated lts, is a quadruple (S, \rightarrow, T, s_0) where S is a set of *states*, T is a set of *labels* with $S \cap T = \emptyset$, $\rightarrow \subseteq (S \times T \times S)$ is the *transition relation*, and $s_0 \in S$ is an *initial state*.³ For a label $x \in T$, the *restricted lts* $TS-x$ is the lts $(S, \{(s, t, s') \in \rightarrow \mid t \neq x\}, T \setminus \{x\}, s_0)$ obtained by dropping all x -labelled transitions.

A label t is *enabled* in a state s , denoted by $s[t]$, if there is some state $s' \in S$ such that $(s, t, s') \in \rightarrow$, and *backward enabled* in s , denoted by $[t]s$, if there is some state $s'' \in S$ such that $(s'', t, s) \in \rightarrow$. For $s \in S$, let $s^\bullet = \{t \in T \mid s[t]\}$. For $t \in T$, $s[t]s'$ iff $(s, t, s') \in \rightarrow$, meaning that s' is *reachable* from s through the execution of t . The definitions of enabledness and of the reachability relation are extended to sequences $\sigma \in T^*$:

$s[\varepsilon]$ and $s[\varepsilon]s$ are always true;
 $s[\sigma t] (s[\sigma t]s')$ iff there is some s'' with $s[\sigma]s''$ and $s''[t] (s''[t]s')$, respectively).

For any $s \in S$, $[s] = \{s' \in S \mid \exists \sigma \in T^* : s[\sigma]s'\}$ denotes the set of states reachable from s . Two lts with the same label set, (S, \rightarrow, T, s_0) and $(S', \rightarrow', T, s'_0)$, will be called *isomorphic* if there is a bijection $\beta: S \rightarrow S'$ such that $s'_0 = \beta(s_0)$ and $(r, t, s) \in \rightarrow$ iff $(\beta(r), t, \beta(s)) \in \rightarrow'$.

³ Formally, S can be considered as a set of vertices and \rightarrow as a set of edges of a directed graph whose edges are labelled by letters from T .

For a finite sequence $\sigma \in T^*$ of labels, the *Parikh vector* $\Psi(\sigma)$ is a T -vector (i.e., a vector of natural numbers with index set T), where $\Psi(\sigma)(t)$ denotes the number of occurrences of t in σ . $s[\sigma]s'$ is called a *cycle*, or more precisely a *cycle at (or around) state s* , if $s = s'$. The cycle is *nontrivial* if $\sigma \neq \varepsilon$. An lts is called *acyclic* if it has no nontrivial cycles. A nontrivial cycle $s[\sigma]s$ around a reachable state $s \in [s_0]$ is called *small* if there is no nontrivial cycle $s'[\sigma']s'$ with $s' \in [s_0]$ and $\Psi(\sigma') \not\leq \Psi(\sigma)$, where, by definition, \leq equals $(\leq \cap \neq)$. An lts has property **P1** [3] if the Parikh vector of any small cycle in TS contains each transition exactly once. \square 4

If there is some sequence $s[\tau]s'$ leading from state s to state s' , we sometimes wish to emphasise the intermediate states. In that case, we also express this, more explicitly, by an alternating sequence

$$s = q_0 t_1 q_1 t_2 \dots q_{n-1} t_n q_n = s'$$

meaning that $\tau = t_1 \dots t_n$ leads from s to s' while visiting the intervening states q_0, q_1, \dots, q_n , in that order.

Definition 5. BASIC PROPERTIES OF AN LTS

A labelled transition system (S, \rightarrow, T, s_0) is called

- *totally reachable* if $[s_0] = S$ (i.e., every state is reachable from s_0);
- *finite* if S and T (hence also \rightarrow) are finite sets;
- *deterministic*, if for any states $s, s', s'' \in [s_0]$ and sequences $\sigma, \tau \in T^*$ with $\Psi(\sigma) = \Psi(\tau)$: $(s[\sigma]s' \wedge s[\tau]s'') \Rightarrow s' = s''$ and $(s'[\sigma]s \wedge s''[\tau]s) \Rightarrow s' = s''$ (i.e., from any one state, Parikh-equivalent sequences may not lead to two different successor states, nor come from two different predecessor states);
- *reversible* if $\forall s \in [s_0]: s_0 \in [s]$ (i.e., s_0 always remains reachable);
- *persistent* [6] if for all reachable states s, s', s'' , and labels t, u , if $s[t]s'$ and $s[u]s''$ with $t \neq u$, then there is some (reachable) state $r \in S$ such that both $s'[u]r$ and $s''[t]r$ (i.e., once two different labels are both enabled, neither can disable the other, and executing both, in any order, leads to the same state);
- *backward persistent* if for all reachable states s, s', s'' , and labels t, u , if $s'[t]s$ and $s''[u]s$ and $t \neq u$, then there is some reachable state $r \in S$ such that both $r[u]s'$ and $r[t]s''$ (i.e., persistence in backward direction). \square 5

If the lts is totally reachable, reversibility is the same as strong connectedness in the graph-theoretical sense.

Definition 6. PETRI NETS, MARKINGS, REACHABILITY GRAPHS

A (finite, initially marked, place-transition, arc-weighted) Petri net is a tuple $N = (P, T, F, M_0)$ such that P is a finite set of *places*, T is a finite set of *transitions*, with $P \cap T = \emptyset$, F is a *flow function* $F: ((P \times T) \cup (T \times P)) \rightarrow \mathbb{N}$, M_0

is the *initial marking*, where a *marking* is a mapping $M: P \rightarrow \mathbb{N}$. A transition $t \in T$ is *enabled by* a marking M , denoted by $M[t]$, if for all places $p \in P$, $M(p) \geq F(p, t)$. If t is enabled at M , then t can *occur* (or *fire*) in M , leading to the marking M' defined by $M'(p) = M(p) - F(p, t) + F(t, p)$ (denoted by $M[t]M'$). The set of markings reachable from M is denoted $[M]$. The *reachability graph of N* , $RG(N)$, is the labelled transition system with the set of vertices $[M_0]$ and set of edges $\{(M, t, M') \mid M, M' \in [M_0] \wedge M[t]M'\}$. A Petri net N *solves* a transition system TS , if $RG(N)$ and TS are isomorphic. \square 6

Definition 7. BASIC STRUCTURAL PROPERTIES OF PETRI NETS

For a place p of a Petri net $N = (P, T, F, M_0)$, let $\bullet p = \{t \in T \mid F(t, p) > 0\}$ and $p^\bullet = \{t \in T \mid F(p, t) > 0\}$. N is called *connected* if it is weakly connected as a graph; *plain* if $\text{cod}(F) \subseteq \{0, 1\}$; *pure* or *side-condition free* if $p^\bullet \cap \bullet p = \emptyset$ for all places $p \in P$; and a *marked graph* if N is plain and $|p^\bullet| \leq 1$ and $|\bullet p| \leq 1$ for all places $p \in P$. For $t \in T$ and $w \in T^*$, let $\#_t(w) = \Psi(w)(t)$ denote the number of times transition t occurs in w . The *effect* of a sequence w on a place p is

$$ef_p(w) = \sum_{t \in \bullet p} \Psi(w)(t) - \sum_{t \in p^\bullet} \Psi(w)(t)$$

i.e., the token difference w would generate on p if it were executed. For $w \in T^*$, define $\blacklozenge w = \{p \in P \mid ef_p(w) < 0\}$ and $w^\blacklozenge = \{p \in P \mid ef_p(w) > 0\}$. \square 7

For the effect of $w \in T^*$ on a given place p , there are exactly three, mutually exclusive, possibilities:

- $\sum_{t \in T} F(p, t) \cdot \#_t(w) < 0$. In that case, the effect of w on p is negative.
- $\sum_{t \in T} F(p, t) \cdot \#_t(w) > 0$. In that case, the effect of w on p is positive.
- $\sum_{t \in T} F(p, t) \cdot \#_t(w) = 0$. In that case, the effect of w on p is neutral.

In a pps net, when executing a sequence $M[w]M'$ from a marking M to a marking M' , this specialises as follows, again with respect to a given place p :

- $\#_{p^\bullet}(w) = \#_{\bullet p}(w) + 1$. In that case, $M(p) = 1$ and $M'(p) = 0$, and the effect of w on p is negative, more precisely, equal to -1 .
- $\#_{p^\bullet}(w) = \#_{\bullet p}(w) - 1$. In that case, $M(p) = 0$ and $M'(p) = 1$, and the effect of w on p is positive, more precisely, equal to $+1$.
- $\#_{p^\bullet}(w) = \#_{\bullet p}(w)$. In that case, $M(p) = M'(p)$, and the effect of w on p is neutral, more precisely, equal to 0 .

In general, let $\blacklozenge w$ (respectively, w^\blacklozenge) denote the sets of places on which w has a negative (respectively, positive) effect. Note that $\blacklozenge \varepsilon = \emptyset = \varepsilon^\blacklozenge$, since ε acts neutrally on any place. Also, if $\Psi(w_1) = \Psi(w_2)$, then $\blacklozenge w_1 = \blacklozenge w_2$ and $w_1^\blacklozenge = w_2^\blacklozenge$. This follows directly from the definitions, since for any place p , the number of transitions of $\bullet p$ is the same in w_1 as in w_2 , and similarly for the transitions in

p^\bullet . Moreover, if $M[uv]M'$, then for every place $p \in P$ the effect of uv on p is equal to the effect of w on p plus the effect of v on p ($ef_p(uv) = ef_p(w) + ef_p(v)$). Also, if w' and v' are such that $\Psi(w) = \Psi(w')$ and $\Psi(v) = \Psi(v')$ then $ef_p(uv) = ef_p(w') + ef_p(v')$.

In the pps case, because of plainness and pureness, the \blacklozenge notation is consistent with the pre- and postset notation, more precisely: $\blacklozenge w = \bullet a$ and $w \blacklozenge = a^\bullet$ provided $w = a \in T$.

Definition 8. BASIC BEHAVIOURAL PROPERTIES OF PETRI NETS

A Petri net $N = (P, T, F, M_0)$ is *weakly live* if $\forall t \in T \exists M \in [M_0]: M[t]$ (i.e., there are no unfireable - hence dead, hence irrelevant - transitions); *k-bounded* for some fixed $k \in \mathbb{N}$, if $\forall M \in [M_0] \forall p \in P: M(p) \leq k$ (i.e., the number of tokens on any place never exceeds k); *safe* if it is 1-bounded; *bounded* if $\exists k \in \mathbb{N}: N$ is k -bounded; *persistent (reversible)* if its reachability graph is persistent (reversible, respectively); and *live* if $\forall t \in T \forall M \in [M_0] \exists M' \in [M]: M[t]$ (i.e., no transition can be made unfireable). Finally, N is called *pps* if it is plain, pure, and safe. □ 8

Proposition 5. PROPERTIES OF PETRI NET REACHABILITY GRAPHS

The reachability graph RG of a Petri net N is totally reachable and deterministic. N is bounded iff RG is finite. □ 5

The class of pps (plain, pure, safe) Petri nets specified in definition 8 is very much related to *elementary nets* [10], in the following way. Elementary Petri nets have a strengthened firing rule: viz., t can occur if all its pre-places have exactly one token *and* all its post-places have exactly zero tokens. Every elementary net with this strengthened firing rule can be turned into an equivalent pps net with the usual firing rule, by adding appropriate complement places. Conversely, for pps nets, the two firing rules coincide.