IMPLEMENTATION OF DIFFERENCE SOLUTIONS OF MAXWELL'S EQUATIONS ON THE GPU BY METHOD OF PYRAMID

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Abstract. The work is dedicated to the development of the method of pyramids in the annex to the decision of Maxwell's equations in the time domain by means of the finite difference (FDTD) and its implementation on the GPU. This method allows you to remove the restriction on the amount of memory the graphics computing device.

Keywords: FDTD, Maxwell's equations, method of pyramids, GPU, CUDA.

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Introduction

Difference solution of Maxwell's equations method in the time domain (FDTD) [1] is widely used in modern research in the simulation: the propagation of radiation [2], materials with new properties [3], the interaction of radiation with biological objects [4] and in many other applications.

Its popularity is due to methodological clarity (based on the replacement of derivatives by difference quotients), wide scope of application (rigorous theory of diffraction) and an abundance of ready-made software implementations (many commercial and open source packages for different architectures and operating systems). The price for the universality of the method - high demands on processor speed and amount of RAM. Therefore developed half a century ago, the method has been actively used in computational practice until now.

The development of computer technology hardware base faced with difficulties ("silicon dead end"). It is not by increasing clock speed of processors. Produces special architectural solutions. They allow to increase the speed of certain types of operations. The most notable successes achieved in this direction during the development of the architecture of graphics processors (GPU) and related software tools (CUDA [5], the OpenCL [6]), allowing multiple accelerate action with vectors and matrices.

From a mathematical point of view, based on the FDTD method is an operation of subtraction of matrices. Modern implementations FDTD-method on GPU [7, 8] characterized by an increase in productivity of 10 times compared to the central computing processor. However, a limited amount of video memory (2-6 Gb. compared with 4-32 Gb. RAM) does not allow full use of the advantage of GPU performance.

Overcoming this limitation - an urgent task. To solve this problem in this paper, we propose to apply the pyramid method [9,10,11]. It is based on reducing the intensity of communication between RAM and VRAM due to duplication of arithmetic operations.

A similar problem applied to other differential equations of the previously successfully solved in the works [12,13,14].

Yee algorithm

Illustrating the application of the method of the pyramids to the times-difference solution of Maxwell's equations, the authors settled on the classical Yee scheme for the three-dimensional case [15], which is currently the most popular tool for computational electrodynamics [1]. The main feature of this scheme is to separate the location of the nodes of the mesh region for each projection of the electric and magnetic fields, providing increased order of approximation of the initial differential problem by time and space.

Α	d	M_t	а	I_x	b	J_y	С	Kz
E_x			0,5	1				
E_y					0,5	1		
E_{z}							0,5	1
H_{x}	0,5	1			0,5	1	0,5	1
H_y	0,5	1	0,5	1			0,5	1
H_{z}	0,5	1	0,5	1	0,5	1		

Table 1. The coefficients are grid to projections of the electric and magnetic fields

We considered three-dimensional space of the area of computational experiment D^3 ($0 < t \le T$, $0 \le x \le L_x$, $0 \le y \le L_y$, $0 \le z \le L_z$) the grid structure is superimposed on the area D_h^3 . In the nodes { $(t_{m+d}, x_{i+a}, y_{j+b}, z_{k+c})$: $t_{m+d} = (m+d)h_t$, $m = 0,1,..., M - M_t$, $(M = T / h_t)$, $x_{i+a} = (i+a)h_x$, $i = 0,1,..., I - I_x$, $(I = L_x / h_x)$, $y_{j+b} = (j+b)h_y$, $j = 0,1,..., J - J_y$, $(J = L_y / h_y)$, $z_{k+c} = (k+c)h_z$,

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 $k = 0, 1, ..., K - K_z$, $K = L_z / h_z$ of grid defined projection of the field $A_{i+a,j+b,k+c}^{m+d}$. The values of coefficients for different grid projections of electric and magnetic fields are presented in table 1. The empty cells correspond to zero value of the coefficient. The difference scheme for this region is represented by the equations:

$$H_{x_{i,j+0.5,k+0.5}}^{m+0.5} = D_{a_{i,j+0.5,k+0.5}} H_{x_{i,j+0.5,k+0.5}}^{m-0.5} - D_{b_{i,j+0.5,k+0.5}} \left(\frac{E_{z_{i,j+1,k+0.5}}^m - E_{z_{i,j,k+0.5}}^m}{h_y} - \frac{E_{y_{i,j+0.5,k+1}}^m - E_{y_{i,j+0.5,k+1}}^m}{h_z} \right)$$

$$H_{y_{i+0.5,j,k+0.5}}^{m+0.5} = D_{a_{i+0.5,j,k+0.5}} H_{y_{i+0.5,j,k+0.5}}^{m-0.5} - D_{b_{i+0.5,j,k+0.5}} \left(\frac{E_{x_{i+0.5,j,k+1}}^m - E_{x_{i+0.5,j,k}}^m}{h_z} - \frac{E_{z_{i+1,j,k+0.5}}^m - E_{z_{i,j,k+0.5}}^m}{h_x} \right)$$

$$H_{y_{i+0.5,j,k+0.5}}^{m+0.5} = D_{a_{i+0.5,j,k+0.5}} H_{y_{i+0.5,j,k+0.5}}^m - D_{b_{i+0.5,j,k+0.5}} \left(\frac{E_{y_{i+1,j+0.5,k}}^m - E_{y_{i,j+0.5,k}}^m}{h_z} - \frac{E_{z_{i+1,j,k+0.5}}^m - E_{z_{i,j,k+0.5}}^m}{h_x} \right)$$

$$H_{z_{i+0.5,j+0.5,k}}^{m+0.5} = D_{a_{i+0.5,j+0.5,k}} H_{z_{i+0.5,j+0.5,k}}^{m-0.5} - D_{b_{i+0.5,j+0.5,k}} \left(\frac{y_{i+1,j+0.5,k}}{h_x} - \frac{y_{i,j+0.5,k}}{h_y} - \frac{y_{i+0.5,j+1,k}}{h_y} \right)$$

$$E_{x_{i+0.5,j,k}}^{m+1} = C_{a_{i+0.5,j,k}} E_{x_{i+0.5,j,k}}^{m} + C_{b_{i+0.5,j,k}} \left(\frac{H_{z_{i+0.5,j+0.5,k}}^{m+0.5} - H_{z_{i+0.5,j-0.5,k}}^{m+0.5}}{h_y} - \frac{H_{y_{i+0.5,j,k+0.5}}^{m+0.5} - H_{y_{i+0.5,j,k-0.5}}^{m+0.5}}{h_z} \right)$$

$$E_{y_{i,j+0.5,k}}^{m+1} = C_{a_{i,j+0.5,k}} E_{y_{i,j+0.5,k}}^{m} + C_{b_{i,j+0.5,k}} \left(\frac{H_{x_{i,j+0.5,k+0.5}}^{m+0.5} - H_{x_{i,j+0.5,k-0.5}}^{m+0.5}}{h_z} - \frac{H_{z_{i+0.5,j+0.5,k}}^{m+0.5} - H_{z_{i+0.5,j+0.5,k}}^{m+0.5}}{h_x} \right)$$

$$E_{z_{i,j,k+0.5}}^{m+1} = C_{a_{i,j,k+0.5}} E_{z_{i,j,k+0.5}}^m + C_{b_{i,j,k+0.5}} \left(\frac{H_{y_{i+0.5,j,k+0.5}}^{m+0.5} - H_{y_{i-0.5,j,k+0.5}}^{m+0.5}}{h_x} - \frac{H_{x_{i,j+0.5,k+0.5}}^{m+0.5} - H_{x_{i,j-0.5,k+0.5}}^{m+0.5}}{h_y} \right)$$

Where,
$$C_{a_{i,j,k}} = \frac{1 - \frac{\sigma_{i,j,k}h_t}{2\varepsilon_{i,j,k}\varepsilon_0}}{1 + \frac{\sigma_{i,j,k}h_t}{2\varepsilon_{i,j,k}\varepsilon_0}}$$
, $C_{b_{i,j,k}} = \frac{\frac{h_t}{\varepsilon_{i,j,k}\varepsilon_0}}{1 + \frac{\sigma_{i,j,k}h_t}{2\varepsilon_{i,j,k}\varepsilon_0}}$, $D_{a_{i,j,k}} = \frac{1 - \frac{\sigma^*_{i,j,k}h_t}{2\mu_{i,j,k}\mu_0}}{1 + \frac{\sigma^*_{i,j,k}h_t}{2\mu_{i,j,k}\mu_0}}$

 $D_{b_{i,j,k}} = \frac{\frac{h_t}{\mu_{i,j,k} \mu_0}}{1 + \frac{\sigma_{i,j,k}^* h_t}{2\mu_{i,j,k} \mu_0}} \cdot \varepsilon_0, \ \mu_0 \text{ it electric and magnetic constants, } \varepsilon_{i,j,k}, \ \mu_{i,j,k} \text{ it grid}$

of the relative electrical and of the magnetic permittivity of linear isotropic environment without dispersion, $\sigma_{i,j,k}$, $\sigma^{*}_{i,j,k}$ it grid-specific of the electric and of the magnetic conductivity.

Software implementation of FDTD-method

Consider the propagation of a plane wave in the three-dimensional region of space, bounded absorbing layers CPML [1]. We consider the Maxwell equations and difference scheme Yee for them [1]. The solution of difference equations is a means of modeling.

Define the grid area the size of $256 \times 256 \times 256$ samples in space. The width of the absorbing layer select equals 11 counts. It should be 467 Mb to accommodate such a task in the memory card.

We apply the method to a one-dimensional pyramid decomposition [12] and carry out the partition of the original grid area of one chosen direction. The following is the author's code to CUDA C to Yee algorithm in [1]:

```
_host___ void raschetGPU_pyramid_1d()
{
 // The number of launched blocks
 int SIZEx = ((Imax-1)%BLOCK DIMx==0) ?
 (Imax-1)/BLOCK DIMx : (Imax-1)/BLOCK DIMx+1;
 int SIZEy = ((Jmax-1)%BLOCK_DIMy==0)
                                       ?
  (Jmax-1)/BLOCK DIMy : (Jmax-1)/BLOCK DIMy+1;
 dim3 gridSize = dim3(SIZEx,SIZEy, 1);
 dim3 blockSize = dim3(BLOCK DIMx, BLOCK DIMy,
 BLOCK DIMz);
 create_temp_fields();
 // Copying of overlapping regions into a temporary
 // buffer
 int countPyramidsK = ceil((Kmax-1) /
  (pyramidBaseLengthK + 0.0f));
 // The number of pyramids
 int currentTime = 1;// The current step of time
 int timeOfPass =((nmax)%PASS==0)?
 nmax/PASS : nmax/PASS+1;
 // The number of passes
 // The loop of passes
 for (int pass = 1; pass <= PASS; pass++)</pre>
  int currentBaseLengthK; // the width of the upper base
  //of the current pyramid
  int leftOffsetK; // the width of the left overlap of
  // the bottom base of the pyramid
  int rightOffsetK; // the width of the right overlap of
  //the bottom base of the pyramid
  int fullBaseLengthK; // the width of the bottom base of
  //the current pyramid
  int leftPyramidBorderK; // the index of the left
  // border of the base of the pyramid
  int rightPyramidBorderK;// the index of the right
  // border of the base of the pyramid
```

```
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   int durationOfTimePass = timeOfPass * pass; // the
   // time step corresponding to the end of the current
   // passage
   // Copying into a temporary buffer initial field
   //values to pass
   copyFields(...);
   // The loop for pyramid
   for(int pyramidIdK = 0; pyramidIdK < countPyramidsK;</pre>
   pyramidIdK++)
    int startPyramidBasePositionK = pyramidIdK *
    pyramid BaseLengthK; // The starting index of the
    //upper base of the pyramid in the grid
    getOffsets(pyramidIdK, pyramidBaseLengthK,
    &currentBaseLengthK, &leftOffsetK, &rightOffsetK);
    getBorders(currentBaseLengthK, leftOffsetK,
    rightOffsetK, &fullBaseLengthK, &leftPyramidBorderK,
    &rightPyramidBorderK);
    // Copying fields on a graphics card
    create arrays on GPU(Imax, Jmax, fullBaseLengthK);
    copy temp arrays to GPU(0,0,leftPyramidBorderK,
    Imax, Jmax, fullBaseLengthK);
    copy constant to device();
    // The loop of time inside passage
    for (int n = currentTime; n <= durationOfTimePass</pre>
    && n <=nmax; n++)
     // Kernel for conversion of magnetic field
     //components on the GPU
     kernelH pyramid1<<<gridSize,blockSize>>>(...);
     cudaEventSynchronize(syncEvent);
     // Kernel for conversion of electric field
     //components on the GPU
     kenelE pyramid1<<<gridSize,blockSize>>>(...);
     cudaEventSynchronize(syncEvent);
    }
   // Copying data from video memory
   copy arrays from GPU with offset(currentBaseLengthK,
   tartPyramidBasePositionK, leftOffsetK);
   delete_arrays_on_GPU();
   currentTime = durationOfTimePass +1;
  }
}
```

Grid subdomain at the lower base of the pyramid (containing the initial data for each pass) intersect. Therefore, these areas are in a temporary buffer (implemented procedure create_temp_fields) must be copied to the beginning of the next pass. Then the values necessary for the processing of the adjacent pyramid will not be lost.

Computational experiments

Numerical calculations to determine the duration of the experiments were carried out on the video card NVIDIA GeForce GTX 660 Ti, and the CPU Intel Core 2 Duo E8500. The study was carried out in the Ubuntu operating system. Exploring the conditions of efficiency of the method of the pyramids will conduct a series of computational experiments. Limit the amount of video memory of 245 MB, which will build up to 3 pyramids with different base width and height. The purpose of the experiments - determination dependence on the acceleration of calculations of these parameters.

Fig. 1 shows the results of measuring the duration of computation.



Fig. 1. The duration of the calculation y (ms) as a function of height x (number of samples) of the pyramid for varying the width of the base

Minimum of time calculation is obtained by achieving a balance between reducing the number of communications and increasing the number of arithmetic operations. This is obtained (Fig. 1) using a pyramid base width 64 and height 20 in the total number of time layers equal 256. With further increase of the height of the pyramid calculation time begins to increase due to increased volume of duplicate data. This is illustrated by the U-shaped dependence of the computation time of the height of the pyramid.

Conclusion

Implementation of FDTD method with the use of the pyramids allows to overcome the limitation on the amount of memory of the GPU. This allows you to use the GPU advantage in speed. Pyramid method is based on reducing the intensity of communication between main and video memory by duplicating arithmetic operations.

In applying the method of the pyramids is important to strike a balance between the volume of communication and memory volume. To do this, you must find the optimum width and height of the base of the pyramid.

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