Using Models of Parallel Specialized Processors to Solve the Problem of Signal Separation

V A Zasov

1Samara State Transport University, Svobody street, 2B, Samara, Russia, 443066

e-mail: vzasov@mail.ru

Abstract. This paper considers models of highly efficient specialized processors used for parallel data processing as part of solving the problem of extracting individual signals from an additive mixture of several signals. The proposed models of recursive, nonrecursive, and regularization-based parallel specialized processors provide versatility in solving the problem of signal separation with various algorithms. An advantage of regularization-based processors is that they make the solution stable under conditions where the parameters of objects exhibit expected uncertainty when the inverse problem of signal separation is ill-posed. This paper presents the results we obtained from an asymptotic analysis of the computational complexity involved. The results identify the time it takes to solve problems by using specialized processors. The paper also identifies the conditions for the efficient use of specialized processors.

1. Introduction

The problem of signal separation consists in determining source signals unavailable for direct measurements by using source signals measured in accessible points where the signals are an additive mixture of source signals that are distorted when transmitted.

The computational complexity of algorithms involved in solving that problem is high and, for many applications, is of \( O(N^3) \) order, where \( N \) is the number of signal sources [1]. This makes it difficult to use these algorithms.

Computation parallelization is the conventional approach to reducing the time it takes to solve the problem of signal separation [2,3]. Parallel algorithms for signal separation have been developed for multicore processors, multiprocessor systems with shared and distributed memory, and multicomputer systems [4]. That solution is needed in many practical fields such as monitoring and diagnosis of technical facilities [5], communications, medical diagnosis, speech [6] and image [7] processing.

This is because in complicated facilities, measured signals present an additive mixture of signals received from many components, and in most practical applications the extraction of parameters that describe the state of specific components is impossible without signal separation.

The next significant performance improvement is possible through the use of specialized processors whose architecture and computational processes correspond most to the structure of the algorithm for the class of problems in question [1,8].

Signal separation methods can be classified into two groups—deterministic and statistical [1].

The deterministic group is based on principal information about signal transmission channels (statistical, frequency, amplitude, and other channel characteristics); that is, transmission channels and
signals are known.

The statistical group is based on principal information about signal sources such as lacking source correlation and the knowledge of signal distribution laws. In this case, explicit information about transmission channels is unavailable, and only observed signals are known. For that reason, the methods within this group are often called “blind” [9].

Thus, the solution to the problem of separating of signal sources reduces to using a deterministic or statistical method to calculate the separating matrix equal or close, in terms of specific criteria, to the matrix inverse to mixing matrix.

The functionality of commercially available specialized digital signal processors and field-programmable gate arrays is insufficient for solving the complex problem of signal separation.

Reference [10] only proposes basic signal-separation functions and objectives for specialized processors used for signal separation and restoration; and for the processor models described in [11], the analysis of the computational complexity involved in parallel processing is inadequate to identify the conditions for the efficient use of the processors.

It is advisable that the structure of specialized processors should be regular and have a neural network architecture [12]. Besides, those processors do not provide stable solutions under conditions where the properties of objects exhibit expected uncertainty when the inverse problem of signal separation is ill-posed.

The purpose of this paper is to develop parallel specialized processors for signal separation under conditions where the parameters of objects exhibit expected uncertainty and to analyze asymptotically the computational complexity of parallel processing to identify the conditions for the efficient use of the processors.

2. Research Area

Since there are many algorithms for solving the problem of signal separation [1,9-13], parallel specialized processors should provide versatility in this class of problems. We will assume that the processor model consists of two units: the generic unit, which carries out the algorithm’s procedure steps; and the specialized unit, which provides structural simulation for the algorithm.

Let us assume that the model of signal formation is a linear multidimensional system with \( N \) inputs and \( M \) outputs [1,14]. The model’s input signals are \( s_n(k), \) \( n=1,2,\ldots,N; \) its output signals, \( x_m(k), \) \( m=1,2,\ldots,M. \) The input signals come from a variety of sources unavailable for direct measurement, and the output signals come from various receivers such as detectors and antennas. We will assume that each output \( M \) is linked with all the \( N \) inputs through linear signal-transmission channels.

The mathematical model of signal formation is described by discrete-convolution equations (1), where the \( m \) th observable signal is the additive mixture of channel-distorted source signals and noise [1,14] - that is,

\[
x_m(k) = \sum_{n=1}^{N} \sum_{g=0}^{G-1} h_{nm}(g,I) s_n(k-g) + y_m(k),
\]

(1)

where \( h_{nm}(g,I) \) is the element \( N \times M \) of the mixing matrix \( h(g,I) \) for the channels’ pulse responses; \( y(k) \) is the noise vector; \( g=0,\ldots,G-1 \) and \( k=0,\ldots,K-1 \) are the samples of the pulse responses for channels and signals, respectively.

Let us assume that the channels’ pulse responses \( h_{nm}(g,I) \) are finite and that they depend on a certain parameter vector, \( I \) (time, locations of sources and receivers in relation to one another, etc.) [14].

Generally, the solution to the problem of separating source signals is (1), and it can be written as

\[
\tilde{s}_n(k) = \sum_{m=1}^{M} \sum_{g=0}^{G-1} w_{nm}(g,I) x_m(k-g),
\]

(2)

where \( w_{nm}(g,I) \) are the pulse responses of separating filters, and they form the separating matrix.
\( w(g, I) \), which is equal or close to a matrix inverse to the matrix \( h(g, I) \).

In separating signals by source, we will split the computation into two steps.

In step 1, the elements \( w_{mn}(g,I) \) of separating matrix \( w(g, I) \) are determined from the measured dynamical properties of channels or from signal parameters, and the signal-separation algorithm is adjusted. The algorithm for computing \( w_{mn}(g,I) \) (the adjustment algorithm) takes into account the nuances of the given signal-separation algorithm.

In step 2, the signals are separated with the digital separating filters adjusted in step 1. This computational process is the same for different signal-separation algorithms.

With this in mind, we will look at two computational units in the model of a parallel specialized processor: the adjusting processor (AP) (the versatile unit) and the functional processor (FP) (the specialized unit).

The nonrecursive, recursive and regularization-based processor models treated below differ in the methods used to solve (1) but feature the same basic elements that the models are based on.

3. Model for a Nonrecursive Parallel Specialized Processor

The model for the nonrecursive parallel specialized processor implements the method used to solve the system of equations (1) by inversing the mixing matrix \( h(g, I) \).

The nonrecursive processor model \([10,11]\) is best written in the form convenient for parallel stream processing in the time domain:

\[
\hat{s}_n(k) = \sum_{m=1}^{M} \sum_{g=0}^{G-1} w_{nm}(g,I) x_m(k-g) 
\]

\[
\hat{s}_n(k) = \sum_{m=1}^{M} \sum_{g=0}^{G-1} w_{nm}(g,I) x_m(k-g) 
\]

where \( \hat{s}_n(k) \), \( s_n(k) \) are the calculated signals that are approximations (samples) of the true signals \( s_n(k) \) in the points they are formed in; and \( w_{nm}(g,I) \) are the elements of separating matrix \( w(g, I) \) obtained from

\[
w_{nm}(g,I) = \frac{1}{K} \sum_{k=0}^{K-1} W_{nm}(\omega_g, I) \cdot \exp \left( i \frac{2\pi k \omega_g}{K} \right).
\]

The frequency transmission factor \( W_{nm}(\omega_g, I) \) is an element located at the crossing of the \( n \)th row and the \( m \)th column of the spectral matrix \( W(\omega_g, I) \), which is inverse of the \( H(\omega_g, I) \) matrix—that is, \( W(\omega_g, I) = H^{-1}(\omega_g, I) \) at \( M=N \) (or of the pseudo-inverse matrix \( W(\omega_g, I) = H^+(\omega_g, I) \) at \( M\neq N \)).

The functional processor separates the source signals \( x_m(k) \) according as they belong to the sources \( s_n(k) \). This processor implements a model that is inverse of the signal-formation model, and the processor has the regular homogeneous structure composed of \( N \times M \) adjustable filters (AF) and \( N \) adders (A).

The filters and the units compute linear convolutions simultaneously and independently of one another. The computational complexity \( L_{\text{nonrec}}^{\text{FP}}(K) \) of the functional processor’s operation algorithm, which determines the processing time, is characterized by the height of its parallel structure, is of \( L_{\text{nonrec}}^{\text{FP}}(K) = O(K) \) order, and does not depend on the number of signal sources.

The adjustment processor calculates the coefficients \( w_{nm}(g,I) \) for the AFs from the measurements of the channels’ transient responses (deterministic methods) or from the characteristics of signal sources.
(statistical separation methods) [9,13].

For deterministic methods, the algorithm used to compute the coefficients \( w_{nm}(g,I) \) consists of the following steps: using a fast Fourier transform for the channels’ transient responses to obtain the mixing spectral matrix \( H(\omega,I) \); inverting the spectral matrix \( H(\omega,I) \) to obtain the separating spectral matrix \( W(\omega,I) \); and using an inverse fast Fourier transform (IFFT) for the elements of separating matrix \( W(\omega,I) \) to obtain weight coefficients for the AFs, specified by the matrix \( w(g,I) \).

The parallel form of the adjustment algorithm’s first and third steps has a width of \( N \times M \) and is implemented by \( N \times M \) units of the fast Fourier transform and the inverse fast Fourier transform. The computational complexity \( L_{nonrec}^{AP,3}(G) \), which determines the time it takes to complete these steps depending on the heights of their parallel structures, is of \( L_{nonrec}^{AP,3}(G) = O(G \log_2 G) \) order.

The parallel form of the algorithm used to compute the separating matrix has a width of \( G \) and is implemented with \( G \) units for inverting \( N \) order matrices (assuming that \( N=M \)). Each of these units, in turn, implements the parallel form of the algorithm used to compute the inverting matrix (e.g., [3]) with a width of \( O(N^3) \).

The height \( L_{nonrec}^{AP2}(N) = O(\log_2^2 N) \) of the algorithm’s parallel form determines the time it takes to invert the spectral matrix \( H(\omega,I) \).

The computational complexity \( L_{nonrec}^{AP,2,3}(G,N) \) of the adjustment processor’s operation algorithm is significantly higher than the computational complexity \( L_{rec}^{FP} = O(K) \) of the functional processor’s operation algorithm—that is,

\[
O(G \log_2 G) + O(\log_2^2 N) = O(G \log_2 G + \log_2^2 N) > O(K).
\]

For instance, at \( K \approx G \approx N \) the relation \( \frac{L_{nonrec}^{AP,2,3}(G,N)}{L_{rec}^{FP}(K)} \rightarrow \log_2 G \). Thus, separating signals with the proposed nonrecursive processor is acceptable if within the signal interval determined by \( K \approx G \approx N \) the parameters of the mixing matrix are assumed invariable—that is, if the signal-formation model is quasistationary.

Furthermore, given the polynomial relationship between the width \( O(N^4) \) of the parallel form of the matrix-inverting algorithm and the number of signal sources, we can conclude that the model of the nonrecursive processor we discussed can be used in practice to separate the signals \( s_n(k) \) when the number of signal sources is low.

4. Model for a Recursive Parallel Specialized Processor

Figure 1 shows a recursive parallel specialized processor model that implements the iteration method for solving the system of equations (I).

The model’s functional processor (FP) has a regular homogeneous structure composed of \( M \) identical processing units (PU) [10,11]. All PUs operate parallel in time, and each implements the recursive algorithm for extracting one signal \( \hat{s}_n(k) \) from an additive mixture of several signals.

The computational complexity \( L_{rec}^{FP}(K\gamma) \) of the FP operation algorithm, which determines its operating time, is characterized by the height of its parallel form, is of \( L_{rec}^{FP}(K\gamma) = O(K\gamma) \) order (where \( \gamma \) is the number of iterations), and does not depend on the number of signal sources.

The adjusting processor (AP) consists of a clock unit (CU) and \( M \) groups of devices comprising an adjusting unit (AU) and a memory unit (MU). The pulse responses of the filters \( F_{mn}, n=1,...,N \), and \( m=1,...,M \) (note the exclusion of \( m \neq n \)) need not be calculated with the AP since the frequency characteristics of AF \( mn \) filters are equal to the frequency characteristics of channels with related indexes.
in the mixing matrix $\mathbf{H}(\omega, \mathbf{I})$ of the signal-formation model. These characteristics should only be stored in $\mathbf{M}_{m}, \ m=1, \ldots, M$.

The transient responses $\tilde{h}_{11}(g), \tilde{h}_{22}(g), \ldots, \tilde{h}_{MN}(g), \ g=0, \ldots, G-1$ of the adjustable inverse filters $\text{AIF}_{mn}$ (only if $m=n$) are computed in $\mathbf{A}_{m}, \ m=1, \ldots, M$.

The algorithm for computing $\tilde{h}_{mn}(m-n, g, I)$ consists of the following steps: using a fast Fourier transform for the transient responses of the channels $h_{mn}(m-n, g, I)$ to obtain the characteristics $H_{mn}(\omega_g, I)$; computing $\tilde{H}_{mn}(\omega_g, I) = \frac{1}{H_{mn}(\omega_g, I)}$; and using a fast Fourier transform for the characteristics $\tilde{H}_{mn}(\omega_g, I)$ to obtain the weight coefficients $\tilde{h}_{mn}(m-n, g, I)$ for $\text{AIF}_{mn}$.

The parallel form of the adjustment algorithm’s first and third steps has a width of $N$ and is implemented by $N$ FFT and IFFT units.

The computational complexity $L_{\text{rec}}(G)$, which determines the time it takes to complete these steps depending on the height of their parallel form, is of $L_{\text{rec}}(G) = O(G \log_2 G)$ order.

The parallel form of the algorithm used to compute the channels’ inverse characteristics $\tilde{H}_{mn}(\omega_g, I)$ has a width of $O(N \times G)$ and is implemented with $N \times G$ division units, while the height of their parallel form is $O(G)$. The parallel form of the adjustment algorithm has a width of $N$ and is implemented by $N$ FFT and IFFT units.
of the algorithm’s parallel form is a constant.

The CU synchronizes the transmission of parameters from AP to FP, sets the initial conditions, and controls the output registers while completing processing iterations. This unit’s operation algorithm has a constant complexity of \( L_{\text{rec}}^\text{CU}(N) = O(1) \).

The assessment of the computational complexity of the AP and FP algorithms (e.g., at \( K \approx G \)),

\[
\frac{L_{\text{rec}}^{\text{AP}}(G)}{L_{\text{rec}}^{\text{FP}}(K)} = \frac{O(G \log_2 G) + O(1) + O(1)}{O(K^\gamma)} \rightarrow \log_2 G,
\]

for the recursive processor presents the conclusion that signal separation with the proposed recursive processor is acceptable for the quasistationary model of signal formation.

But the width of the AP algorithm’s parallel form for the recursive processor is significantly lower than that of the nonrecursive one: \( O(N \times G) \ll O(N^4 \times G) \).

This advantage of the recursive processor makes it possible to apply the solution to the problem of separating signals \( s_n(k) \) for many more sources under conditions where computational resources are limited.

For a recursive processor to separate signals steadily, the object must allow the receivers of signals to be installed such that in the linear superposition of signals at the outputs of each of the receivers, the signal from a specific source is predominant [1].

5. Model for a Regularization-Based Parallel Specialized Processor

If the parameters of the mixing matrix \( \mathbf{H}(\omega, \mathbf{1}) \) or of the source signals \( s(k) \) make the problem of signal separation ill-posed or if those parameters show expected uncertainty, then one should at once find a regularized, stable solution to (1) or its equivalent in the frequency domain.

The proposed model for a regularization-based specialized processor is based on the Tikhonov regularization [15].

Two conditions to the Tikhonov regularization are set in the processor model: the disparity minimization \( \| \mathbf{Hs} - \mathbf{\hat{x}} \| = \min_s \), as in the least-squares technique (LST); and the minimization of the solution norm, \( \| s \| = \min_s \), as in the Moore–Penrose pseudo-inverse of a matrix [16].

The solution \( s_{\alpha} \) contained in the processor provides the absolute minimum of the smoothing functional \( F^\alpha [s] \) expressed as

\[
F^\alpha [s] = \| \mathbf{Hs} - \mathbf{\hat{x}} \|^2 + \alpha \Omega [s],
\]

where \( \alpha > 0 \) is the regularization parameter; \( \Omega [s] \) is the stabilizing functional; and \( \mathbf{\hat{H}} \) and \( \mathbf{\hat{x}} \) are approximate values of \( \mathbf{H} \) and \( \mathbf{x} \) for which

\[
\| \mathbf{H} - \mathbf{\hat{H}} \| \leq \xi_{\mathbf{H}} \quad \text{and} \quad \| \mathbf{x} - \mathbf{\hat{x}} \| \leq \delta,
\]

where \( \delta \) and \( \xi_{\mathbf{H}} \) are the upper estimates of absolute measurement errors for the signals \( \mathbf{x} \) and the mixing-matrix elements \( \mathbf{H} \).

The proposed model uses \( \Omega [s_{\alpha}] = \| s_{\alpha} \|^2 \) as a stabilizing functional. For control purposes, it is more natural and convenient to present signals in time form, so we will write the smoothing functional for \( M = N \) and \( K = G \) as

\[
F^\alpha [s] = \sum_{m=1}^{M} \sum_{k=0}^{K-1} \left[ \tilde{x}_{m}(k, \mathbf{1}) - x_{m}(k, \mathbf{1}) \right]^2 + \alpha \sum_{n=m+1}^{N-M} \sum_{k=0}^{K-1} \| s_{n,\alpha}(k) \|^2 \quad (3)
\]

The signal \( \tilde{x}_{m}(k, \mathbf{1}) \) in (3) derives from the separation results redistorted by the signal formation model; that is,
\[
\tilde{x}_n(k,I) = \sum_{n=1}^{N} \tilde{s}_{n,a}(k) * h_{nm}(k,I),
\]
where \( \tilde{s}_{n,a}(k,I) \) are the regularized results of signal separation for the object’s \( m \)th node.

Under the conditions described above, the smoothing functional can be written as
\[
F^\alpha[w] = \sum_{m=1}^{M} \sum_{k=0}^{K} \left[ \tilde{x}_m(k,I) - x_m(k,I) \right]^2 + \alpha \sum_{m=1}^{M} \sum_{g=0}^{G} |\tilde{w}_{nm,a}(g,I)|^2,
\]
an expression that is more suitable for the regularized computation of the elements \( \tilde{w}_{nm,a}(g,I) \) for the separating matrix \( \tilde{w}_a(g,I) \), which sets the weights of the functional processor’s AFs.

The elements \( \tilde{w}_{nm,a}(g,I) \) are calculable, for instance, by solving the system of \( M \cdot N \cdot G \) equations written as \( \frac{\partial F^\alpha(\tilde{w})}{\partial \tilde{w}_{nm,a}(g,I)} = 0 \) relative to \( \tilde{w}_{nm,a}(g,I) \), using the parallel algorithms for solving linear algebraic equations [2,3].

For the known (specified) errors \( \xi_m \) and \( \delta \), we propose calculating the regularization parameter as the root of the equation
\[
\|\tilde{H}s_\delta - \tilde{x}\|^2 = \left( \delta + \xi_m \|s_\delta\| \right)^2 / \beta(r,M,N),
\]
in which \( \alpha \) is the parameter \( s_\delta \), where \( \beta(r,M,N) \geq 1 \) is a scalable multiplier determined by the problem’s dimension \( (M \times N) \) and by the measurement error of signal and channel parameters (the error depends, in particular, on the resolution \( r \) of analog-to-digital conversion).

With the regularization parameter \( \alpha \) so obtained, the smoothness and disparity of the solution for \( s_\delta \) are acceptable for practical purposes.

Figure 2 shows the model of a regularization-based specialized processor. The FP is a model that is inverse of the signal-formation model and that separates measured signals. This processor has a homogeneous structure and consists of AFs and AUs.

The computation of regularization parameter \( \alpha \) and the adjustment of AFs, whose number is equal to \( M^2 \), are run by the AP. The AP’s processing unit (PU) computes AF parameters with the least-squares technique through minimizing the smoothing functional \( F^\alpha[w] \).

The elements of the mixing matrix \( \mathbf{h}(g,I) \), which set the samples for the pulse responses of the signal-formation model’s channels, enter the inputs of the AP, and the AP generates a direct signal-formation model.

Disparity evaluation units (DEUs) compute the disparity \( \chi_m^2 \) for each of the processor’s \( m \) channels. The units receive signals delayed by delay units (DUs), which serve to delay signals from the processor inputs and signals from the outputs of the direct signal-formation model.

It is advisable to use deterministic parallel optimization algorithms [17] designed for multicore processors to minimize the smoothing functional \( F^\alpha[w] \).

All the AFs operate in parallel independently of one another. This makes the proposed processor fast and reliable.

The processor model shown in figure 2 is generalized to obtain stable solutions for system (1). For that reason, the model is highly complicated. For practical applications, it can be significantly simplified by using prior information about the signal-formation model (such as the presence of reference inputs [18]) or by using simpler and parallel regularization algorithms [19].
6. Modeling Results

Figure 3 shows the modeling results for test signals separated by the nonrecursive parallel specialized processor with a multicore, GPU-based architecture.

The signal-formation model had three signal sources: the first two were triangular pulses with different frequencies and shapes while the third was a speech signal. The signal receivers used 8-bit ADCs with a sample rate of 12 kHz.

The figure 3 shows the initial signals (top), additive mixtures of signals in each of the receivers (middle), and extraction results for each signal (bottom).

The error of signal separation does not exceed 10%, a value acceptable for many engineering applications.
Figure 3. Modeling results for test signals separated with a nonrecursive parallel specialized processor.

The results of computational experiments shown in figure 4 for the example above show notably shorter task times (the Independent Component Analysis (ICA) [9] algorithm was used).

Figure 4. Experimental time–sample relationships for the problem of signal separation with a serial (a) and parallel nonrecursive specialized (b) processors.
7. Basic Conclusions

This paper proposed using models of highly efficient nonrecursive, recursive and regularization-based parallel specialized processors to solve the problem of signal separation.

Once adjusted, the processors can solve the problem within a period that does not depend on the number of signal sources—that is, the processors have a task time of $T_{fp}(N) = O(1)$ order.

These models are applicable where the parameters of the model-formation model are variable (quasistationary).

Regularization-based processors make the solution stable under conditions where the parameters of objects exhibit expected uncertainty when the inverse problem of signal separation becomes ill-posed.

The regular homogeneous structure of the functional processor can be conveniently implemented as an integrated circuit or a multicore-architecture computational system.

8. References