Theoretical and Experimental Investigation of Error Detecting and Error Correcting Ability of Rank Codes

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Abstract
The paper presents the results of theoretical and experimental studies of the ability of the rank codes, proposed by the authors, to detect and correct errors introduced by information transmission channels. The introduction discusses the principles of rank codes building and their application in decision-making systems. In the main part of the presented work, aspects of research related to the theoretical and experimental evaluation of their ability to detect and correct errors introduced by the transmission channel are highlighted. Algorithms for finding and correcting errors are presented.

Keywords
Methods of unified information description, modeling, rank configurations, DRP-code, encoding, decoding, error detection and correction

1. Introduction

Today, the problem of developing theoretical foundations and practical means of unified presentation of information about the states of control objects, its coding and transmission in control systems during decision-making is quite relevant. It is known that decisions regarding the management of objects of various nature in control systems are made on the basis of information about the distance between the current and target states of the object [1]. These distances in different parametric spaces (deterministic, probabilistic, approximate, fuzzy, etc.) are described differently, which requires for each case the development of separate methods and algorithms for presenting information and making decisions [2, 3, 4, 5]. In order to unify the methods and algorithms of presenting and processing information for decision-making in control systems, the authors proposed to describe the states of the systems by rank configurations, and the rank configurations themselves by potential codes (or DRP codes – codes that preserve the ranks of the distances between states). The proposed codes were called potential by analogy with the electric field. In them, the rank value of the distance between code words is defined as the difference between their values, just as the field strength between electric charges is defined as the difference in their potentials. Such codes make it possible to reduce information processing algorithms in various parametric descriptions to performing a single logical operation on them [6, 7]. To determine the type of this operation and the characteristics of the codes themselves, a number of models of rank configurations, including their interval model, have been developed [6].

Unfortunately, there is a small number of works in the direction of applying such an approach to the description, processing and transmission of information in decision-making systems. As an example, we can note works [8, 9] in which difference codes are proposed for
description and pattern recognition. In [10], we showed the advantages of our approach compared to the one proposed in [8, 9], which makes further research in this area relevant.

2. Analysis of the state of the issue

The codes proposed by us can be used to increase the efficiency of the decision-making process in systems of various purposes [11, 12] and to increase the immunity of the information transmission process in communication channels. For example, in the proposed hierarchical parallel-serial pattern recognition strategy [11], it is convenient to use the description of these objects in a rank scale for the classification of objects at the upper level of the described strategy, since their clusters in the feature space do not intersect.

The authors also developed a system for transmitting rank information through communication channels, proposed schemes for encoding and decoding information in this system without taking into account the influence of interferences on the communication channel [13]. In real network communication channels of computer-integrated control systems, there are intense interference signals that generate errors in code words. Therefore, in this work the task of conducting theoretical and experimental studies is set for the purpose of developing algorithms of localization and correcting asymmetric and symmetric errors introduced by the communication channel in DRP codes.

For a better understanding of the carried out in this work research we present a number of necessary theoretical provisions regarding the models of rank configurations and rank codes (DRP-codes), presented by the authors in the work [6].

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The rank configuration of the space of objects described in some parametric space is called a set of elemental subsets, the elements of these subsets are the ranks of distances incident to the same object [6, 9].

It is convenient to compactly present the rank configuration in algebraic form. For example, the rank configuration in Fig. 1 can be written in algebraic form as

\[ K_i = \{1,4,6\}, \{2,4,5\}, \{2,3,6\}, \{1,3,5\}. \] (1)

The geometric (Fig. 1) and combinatorial (Fig. 2) models of rank configurations are the most relevant for the task of determining the interference resistance and corrective capabilities of the proposed rank codes.

![Figure 1: Geometrical models of rank configurations: a) - three-dimensional simplex, b) - complete regular graph](image)

The simplex and the graph give a visual representation of the rank configuration. In Fig. 1 a) the vertices A, B, C, D denote coded objects, and the numbers on the edges of the simplex and arcs of the graph are their incident ranks.

The use of the graph incidence matrix of Fig.2 a) allows us to directly determine the codes of the A, V, C, D vertices in the DRP-code. It can be seen from Fig. 2a) that this code is by its nature

...
a constant-weight code (CWC), and its bit rate \( n \) is equal to the number of ranks in the rank configuration and is determined by the formula:

\[
n = \frac{m(m - 1)}{2},
\]

where \( m \) is the number of coded elements (graph vertices).

\[
2^{m-1} - 1 \leq (m-1) \cdot \text{rank}
\]

![Figure 2: Combinatorial representation of the rank configuration: a) - incidence matrix of the graph in fig. 1,b); b) – rank adjacency matrix of simplex in fig.1,a)](image)

In the following sections of the work, we will perform theoretical and experimental studies necessary to determine the ability of the proposed rank codes to detect and correct errors introduced by the information transmission channel, and based on them we will develop algorithms for localization and correction of these errors.

3. Theoretical investigation of problem

3.1. Theoretical evaluation of error correcting ability of rank codes

Since the transmission channels of industrial networks, in which the proposed rank code should be used, are subject to the influence of interference capable of generating both symmetric and asymmetric errors, the use of error localization and correction methods used for standard CWC codes is impossible. Accordingly, the theoretical evaluation of the effectiveness of these methods should be carried out taking into account the principles of construction of these codes. According to the characteristics of rank codes, when the number of coded states of systems \( m \) increases, the bit rate \( n \) of the code increases according to the expression (2), and the number of units in the code word also increases and is equal to \( m-1 \).

To obtain a theoretical estimate, we will first prove the theorem: for a rank configuration of dimension \( m \), the Hamming distance \( d_x \) between words of the rank code is equal to \( d_x = 2(m-1)-2 \).

**Proof:** The two code words of the rank code, between which the Hamming distance is calculated, contain \( 2(m-1) \) single digits, and the single digits match only in one digit, one from each word, in total 2. Therefore, the number of digits in which the ones do not match, is equal to \( 2(m-1)-2 \), and the logical XOR operation on these words, by which the Hamming distance is calculated, will give \( 2(m-1)-2 \) units, which is what we needed to prove.

It follows from the proven theorem according to [14] that the theoretical estimate of the possibility of detecting errors by the multiplicity \( g_d \)is determined by the formula

\[
g_d \leq d_x - 1 = 2(m - 1) - 3,
\]

and correction of errors by the \( g_c \) multiplicity - according to the formula:

\[
g_c \leq \frac{(d_x - 1)}{2} = \frac{(2(m - 1) - 3)}{2}.
\]

The identification of the model was performed experimentally on the examples of specific rank configurations of dimensions \( m=4 \) and \( m=5 \) using models of rank configurations in the form of incidence matrices, the rows of which form the DRP-codes of the vertices of the rank simplex.
Figure 3: Evaluation of error detecting and error correction for rank configuration of dimension m=4

\[
\begin{align*}
\delta_x(A, B) &= 000111 \oplus 011001 = 4 \\
\delta_x(A, C) &= 000111 \oplus 101010 = 4 \\
\delta_x(A, D) &= 000111 \oplus 110100 = 4 \\
\delta_x(B, C) &= 011001 \oplus 101010 = 4 \\
\delta_x(B, D) &= 011001 \oplus 110100 = 4 \\
\delta_x(C, D) &= 101010 \oplus 110100 = 4
\end{align*}
\]

\[
\begin{align*}
g_d &\leq \delta_x - 1 = 4 - 1 = 3 \\
g_c &\leq \frac{(\delta_x - 1)}{2} = \frac{3}{2} = 1
\end{align*}
\]

According to the law of induction, it is easy to show numerical values of the multiplicity of detected and corrected errors for rank configurations of higher dimensions \( m=6..11 \). For configurations with a dimension higher than 11, it is inefficient to use potential codes, since their bit rate exceeds the bit rate of modern PC \( n \geq 64 \), and computational operations on the codes become complex and inefficient.

Figure 4: Evaluation of error detecting and error correction for rank configuration of dimension m=5

\[
\begin{align*}
\delta_x(A, B) &= 0001111000 \oplus 0100011100 = 6 \\
\delta_x(A, C) &= 0001111000 \oplus 1000100110 = 6 \\
\delta_x(A, D) &= 0001111000 \oplus 0100010110 = 6 \\
\delta_x(A, E) &= 0001111000 \oplus 1111000000 = 6 \\
\delta_x(B, C) &= 0100011100 \oplus 1111000000 = 6 \\
\delta_x(B, D) &= 1000100110 \oplus 1111000000 = 6 \\
\delta_x(B, E) &= 0100010110 \oplus 1111000000 = 6 \\
\delta_x(C, D) &= 1000100110 \oplus 1111000000 = 6 \\
\delta_x(C, E) &= 1000100110 \oplus 1111000000 = 6 \\
\delta_x(D, E) &= 0100010011 \oplus 1111000000 = 6
\end{align*}
\]

\[
\begin{align*}
g_d &\leq \delta_x - 1 = 6 - 1 = 5 \\
g_c &\leq \frac{(\delta_x - 1)}{2} = \frac{5}{2} = 2
\end{align*}
\]
3.2. Theoretical substantiation of the noise immunity of the rank codes

Before proceeding to experimental studies on the assessment of the ability of the rank code to detect and correct errors introduced by the channel, we will perform a theoretical assessment of the dependence of the DRP code’s reliability on the properties of the transmission channel itself. This will make it possible to focus attention in the experiment on methods of localization and correction of those errors that are most likely to occur in the channel.

The enhanced noise immunity of the proposed DRP-code is based on the fact that it is inherently a constant-weight code (CWC) in which each single bit is placed in the same positions simultaneously in two codewords. The probability of not detecting one error in the code word (the combined probability of converting one zero point into one unit and one unit point into zero) for the CWC, and therefore the DRP-code, is equal to [14]:

\[ P_n = C_{m-1}^1 p(1-p)^{n-2} C_{n-m+1}^1 p(1-p)^{m-2}, \]

where \( m \) is the number of code words, \( p \) – the probability of one error for a symmetric channel, \( n \) - code bit rate. For example, for a rank configuration of four states the number of coded symbols (Fig. 1,a) \( m = 4 \), and the number of ranks of the distances between states (code bit rate) is defined as

\[ n = \frac{m(m-1)}{2} = \frac{4 \cdot 3}{2} = 6. \]

The number of units point in the code word of this configuration is \( (m-1) = 3 \). From the expression (5) we obtain for the DRP-code:

\[ P_{n_{DRP}} = C_4^1 p(1-p)^3 C_4^3 p(1-p)^3 = 9 p^2(1-p)^4. \]

Let’s determine this characteristic for the MTA-3 standard CWC and compare it with (6). For CWC MTA-3, which contains in the code word 3 ones point and 4 zeros point \((m=3, n=7)\) from formula (3) we have:

\[ P_{n_{CWC}} = C_3^1 p(1-p)^3 C_4^4 p(1-p)^4 = 12 p^2(1-p)^6. \]

Taking the value of \( p = 1 \cdot 10^{-4} \), we obtain for the CWC \( P_{n_{CWC}} \approx 12 \cdot 10^{-8} \) and \( P_{n_{DRP}} \approx 9 \cdot 10^{-8} \) for the DRP-code. Since for the DRP-code the unit must not be detected simultaneously in two codewords, then \( P_{n_{DRP}} \approx 81 \cdot 10^{-16} \).

Let’s find a relationship

\[ \frac{P_{n_{CWC}}}{P_{n_{DRP}}} = \frac{12 \cdot 10^{-8}}{81 \cdot 10^{-16}} \approx 0.15 \cdot 10^8, \]

From expression (8) we can see that the reliability of the transmission of rank information using the DRP-code is several orders of magnitude higher than with the standard CWC code. Standard CWC allows only to detect the presence of asymmetric errors, but does not allow to detect symmetric errors, and even more so, to perform their correction. This fact makes it necessary to retransmit information, which slows down the speed of the transmission device.

The conducted theoretical assessment of the DRP code immunity to the influence of symmetric channel interference showed that the probability of introducing a single symmetric error into the DRP code by the channel is quite small, but higher than the probability of multiple type errors. Therefore, in experimental studies, it is necessary to first of all focus efforts on finding methods for building algorithms for localization and correction of single errors.

3.3. Experimental investigation of error correcting ability of DRP codes

According to the standard classification, CWC belongs to block nonlinear codes. The analysis of mathematical models of the rank code shows that it is a set of \( m \) multivalued logical functions of significance \( n \), the values of the arguments of which are obtained by mapping the ordered distances between the elements of the original space to an ordered set of ranks using the logical operation "AND". Since the "AND" operation on a set of code words is not a group one, standard
methods are not suitable for correcting DRP code transmission errors, and the development of original algorithms for localization and error correction based on the study of heuristic patterns is required. Since asymmetric errors in code words (erasure of “1” to “0” or insertion of a “1” instead of “0”) are detected in the CWC by a standard algorithm for counting the number of units in a code word, the main attention will be paid to the study of symmetric errors, i.e. pairwise replacement in certain digits of code words 0" to "1" and vice versa. For research, we will choose the rank configurations of the three-dimensional simplex shown in Fig. 1 a) and DRP-codes, representing by incidence matrix in Fig. 2 a) respectively.

For asymmetric errors in code words, when an additional “1” is introduced, or one “1” is erased to “0”, the localization and correction algorithm is simple:

The number of \( p \) units in the code word is counted, and if \( p > m - 1 \), then the algorithm erases the extra unit in the bit that corresponds to the column of the matrix with the number “1” greater than 2; if \( p < m - 1 \), then the algorithm inserts a “1” into the bit that corresponds to the column of the MKOD matrix, in which there is only one “1”. An example of a DRP code with an additional error introduced by an asymmetric channel in the form of an additional “1” is shown in Table 1.

### Table 1
Asymmetric error in the form of an additional “1” in code word C

<table>
<thead>
<tr>
<th>ranks</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
</tr>
</thead>
<tbody>
<tr>
<td>symbols</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>B</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>C</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>D</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

In Table 1 an additional “1” is entered in the code of symbol C in the 5th digit (this is an asymmetric error). This code word is localized as erroneous by the number of units \( p = 4 > 3 \), and since there are more than two units in the 5th column of the matrix, the localization algorithm and the correction should erase it.

An example of a DRP code with an additional error introduced by an asymmetric channel in the form of an erased “1” is shown in Table 2.

### Table 2
Asymmetric error in the form of an erased “1” in code word A

<table>
<thead>
<tr>
<th>ranks</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
</tr>
</thead>
<tbody>
<tr>
<td>symbols</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>B</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>C</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>D</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

In Table 2, in the 6th digit of the code of symbol A, value “1” is replaced by "0". This code word is localized as false by the number of units \( p = 1 < 3 \), and since there are less than two units in the 6th column of the matrix, the correction algorithm must write a unit in the 6th digit.

The study of symmetric errors will consist in the analysis of the appearance of the DRP code after entering the selected code word with an even error. The number of different variants \( E_v \) of such errors can be determined by the formula:

\[
E_v = m(C^i_{m-1}C^i_{n-m+1}),
\]
where \( m \) is the number of coded symbols (system states), \( C_{m-1} \) - the number of possible errors in units digits, \( C_{(n-m+1)} \) - the number of possible variants of errors in zero digits. For the DRP-code of the configurations selected for the study (Fig. 1 and Fig. 2, \( m=4, n=6 \)), the number of error variants for the study is equal to:

\[
E_v = 4(C_4 \cdot C_6) = 4 \cdot 3 \cdot 3 = 36.
\]

(10)

The analysis of the results of the research for these options showed that in order to build a heuristic algorithm, in addition to the erroneous code words themselves, it is also necessary to analyze the verification matrix \( G[n,n] \) of the rank distances between all pairs of code words. As an example, we determine the distance \( d(AD) \) between the code words of symbols \( A \) and \( D \) in the table of Fig. 2a), for which we use the expression:

\[
d(AD) = x_A \wedge x_D = 101001 \wedge 010101 = 000001,
\]

where \( \wedge \) is the logical operation AND, and \( x_A, x_D \) are DRP-codes of vertices \( A \) and \( D \).

Examples of the correct DRP code and the verification matrix \( G[n,n] \) for them are shown in Fig.5, and the wrong DRP codes and the verification matrices \( G[n,n] \) for them are shown in Fig. 6-7.

**Figure 5:** The correct DRP code of the rank configuration (a) and the verification matrix \( G[n,n] \) of rank distances for it (b)

**Figure 6:** DRP code of the rank configuration (a) with an error in code word B ("1" from digit 5 moved to digit 6, and "0" became in its place) and the check matrix of rank distances for it (b)
Figure 7: DRP code of the rank configuration (a) with an error in code word B (“1” from bit 5 moved to bit 1, and in its place became “0”) and the check matrix $G[n,n]$ of rank distances for it (b)

We will consider those shown in tables 1 and 2, as well as in fig. 5a), 6a) and 7a) DRP codes of words A, B, C and D in the interpretation of some code matrix MKOD. Then symbol A will correspond to the row index $i=1$, symbol B - $i=2$, symbol C - $i=3$, symbol D - $i=4$. Ranks 1, 2, 3, 4, 5, 6 will correspond to the significant index $j$ of the column of the code matrix $j=1$, $j=2$, $j=3$, $j=4$, $j=5$ and $j=6$, respectively.

Distances $d(i,j)$ between code words (for example, $d(AB)$ is $d(1,2)$; $d(AC)$ is $d(1,3)$; $d(BC)$ is $d(2,3)$, etc.) in the verification matrix $G[n,n]$ are determined by the logical operation "AND". This is because in the codeword encoding some vertex of the rank simplex, the fact which rank edge touches it is fixed with the help of "1". For example, edges with ranks 1, 4, 6 touch vertex A, so in code word A there are "1" in code digits 1, 4, 6.

Therefore, in the matrix of MKOD codes for an undamaged code, in each column there are two “1” in the codes of their symbols (vertices of the simplex), which are the incident edge with the rank corresponding to the number of this column, and in the corresponding columns of the verification matrix there is only one “1”, and there is also one “1” in each row of this matrix. These are signs of error-free code.

For the code damaged by a symmetric error in Fig. 7 (where the “1” from the 5th digit moved to the 1st digit, and a "0" was entered in its place), we have in the code matrix MKOD three “1” in the column of the 1st digit and one “1” in the column of the 5th discharge. This is a sign of code damage, since there are still three “1” left in the code words (rows of the MKOD matrix). That is, the code by the number of “1” in the word remains a code with a constant weight of CWC=3, and the number of “1” and the number of "1" in the row of the MKOD matrix is not a feature of introducing a symmetric error into the code word.

In the verification matrix $G[n,n]$ for this case (Fig. 7), we can see that there is no “1” in the 5th digit column (this is a sign to which digit of the damaged word should be returned “1”), and in the 1st digit column there are as many as three “1” (this is a sign in which digit of the damaged word should be returned “0”). Now it remains to determine the index of the damaged word (the row number in the MKOD matrix, in which a single symmetric error is introduced). For this case of the values of the verification matrix $G[n,n]$, the sign is simple - there are two "1"s only in the distance with index 2 ($d(AB)=d(1,2)$), for rows with other indices, the number of "1"s is correct (one “1”). That is, for this case, we determine by this sign that an error was made in the code word B (the second row of the matrix MKOD, i=2) and it is necessary to write "0" in the 1st digit, and in 5th digit "1".

For the code damaged by a single symmetric error in Fig. 6 case for the verification matrix $G[n,n]$ is more complicated. Again, by the number of “1” in its digit columns, we can see that it is necessary to transfer “1” from the 6th digit to the 5th digit of the damaged word, and the
indicator for detecting the number of the damaged word will be the priority of the index in the lines with which the incorrect number of “1” (> one).

Since in the rows of the matrix for \(d(AB), d(BC)\) (respectively \(d(1,2), d(2,3)\)) there are two “1” each, and in the row \(d(BD)\) (respectively \(d(2,4)\)) are all “0”, then the sign here is the presence of the index 2 more than once in these distances (number \(i=2\) row of the MKOD matrix). Therefore, the number of the damaged code word in the MKOD matrix is = 2 (symbol code B), and in it "1" must be transferred from the 6th digit to the 5th.

4. Development of algorithm of error correction in DRP-code

4.1 Verbal description of the error correction algorithm in the rank code

According to the regularities found in the previous sections, the heuristic algorithm for localization and correction of single asymmetric and symmetric errors in the DRP code can have the following verbal description.

In the first step, a two-dimensional array of \(m \times n\) size of \(m\) code words received from channel is formed in the decoder memory, where \(n\) is the bit rate of the code. Under the condition of error-free transmission, this array will correspond to the incidence matrix of the transmitted rank configuration of system states, and each raw of the array will correspond to the correct DRP code word.

In the second step, by counting units in each line of the array, code words with asymmetric errors are localized in the case of \(t \neq m-1\), where \(t\) is the number of units point in the word. Correction of asymmetric errors is carried out according to the procedure: if the number of units \(t=m\) in some word, then the columns of the array of code words are scanned and in the found erroneous word units are replaced with zeros point in those digits whose columns contain more than 2 units point; if the number of units point in some word is \(t<m-1\), then the columns of the array of code words are scanned and in the found erroneous word, zeros point are replaced by units point in those bits whose columns contain less than 2 units point.

In the third step, the columns of the array are scanned and the presence of symmetric even errors is detected in the case of columns with units point, in which the number of units is not equal to 2. Localization of the erroneous code word is carried out using the verification matrix \(G[n,n]\) according to the following procedure: those rows of the matrix that contain two units point are marked. If there is more than one such code word, then the one whose index is contained in the indices of pairs of distances twice is chosen as false.

In the fourth step, the numbers of damaged bits are located and corrected. For asymmetric errors, they are determined by the presence or absence of the necessary units point in the words, and for symmetric errors, as a rule: the column with zeros in the verification matrix of the codeword distances, obtained by the logical operation “AND” over all pairs of codewords, shows a bit with a replacement of \(1 \rightarrow 0\), and the column with three units point is a bit with \(0 \rightarrow 1\) replacement.

By the pseudocode 1, with the selected code matrix \(MKOD\) and verification distance matrix \(G[n,n]\), the algorithm for localization and correction of asymmetric and symmetric errors is shown. To implement the algorithm, a Python program was developed, which is a commercial product and can be provided upon separate request.

**Algorithm 1: detection and correction of errors in DRP-code**

```python
function ASSYMETRIC_ERRORS_CHECK (MKOD)
    \((M, N) \leftarrow MKOD.shape\) \hspace{1cm} \# dimentions of code matrix
    sum_of_rows \leftarrow P \sum MKOD \hspace{1cm} \# get list of sums of rows
    for \((i, sum) \in enumerate(sum_of_rows)\) do
        if sum \neq M - 1 then
```


sum_of_columns ← P \sum MKOD

for (j, sum) ∈ enumerate(sum of columns) do
    if sum > 2 then
        MKOD i,j ← 0
    end if
    if sum < 2 then
        MKOD i,j ← 1
    end if
end for

return MKOD
end function

function SYMETRIC_ERRORS_CHECK(MKOD)
(G, pairs) ← build_check_table(MKOD)

sum_of_rows ← P \sum G

words_with_errors ← " 

for (i, sum) ∈ enumerate(sum of rows) do
    if sum = 2 then
        words_with_errors ← " " .join([words with errors, str(pairs[i,0]), (strpairs[i,1])])
    end if
end for

word_with_error ← int(Counter(words_with_errors) .most_common(1)[0])

sum_of_columns ← P \sum G

for (j, sum) ∈ enumerate(sum of columns) do
    if sum = 0 then
        bit1 ← j
        MKOD[\text{word with error}, j] ← 1
    end if
    if sum = 3 then
        bit2 ← j
        MKOD[\text{word with error}, j] ← 0
    end if
end for

return (MKOD, word_with_error, bit1, bit2)
end function

4.2 Results and discussions

It is rather difficult to investigate with the help of a machine experiment all variants of errors that can be introduced into DRP codes during their transmission over a communication channel, since the number of these variants is quite large. For example, it follows from expression (10) that only for the code that transmits information about only one rank configuration, the number of variants of introduced errors is \( E_v = 36 \). And since for a rank simplex of dimension \( m = 4 \), the number of different rank configurations \( M_r = 30 \) [6], the total of such different variants of errors is equal to \( E_v \cdot M_r = 36 \cdot 30 = 1080 \). For \( m > 4 \), the number of options for processing by a person is unthinkable, and the mechanism of computer generation of various error options in the DRP code at this stage of research has not yet been developed by the authors. Therefore, to check the adequacy of the developed algorithm on the ability to detect and correct errors in rank codes, software testing was conducted, part of the results of which are presented below. The results
show that the developed algorithm correctly localizes and corrects both asymmetric and symmetric errors in codes.

Start of asymmetric errors check:
input matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1 \\
1 & 0 & 1 & 1 & 0 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
sum of rows = [3 3 4 3]
index of corrupted word = 3
code of corrupted word = [1 1 0 1 1 0]
corrupted bit = 6
fixed matrix:
\[
\begin{bmatrix}
1 & 0 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
End
Fixed!

Start of symmetric errors check:
input matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 1 & 0 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
check matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 1 \\
0 & 0 & 0 & 0 & 1 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0
\end{bmatrix}
\]
index of corrupted word = 1
code of corrupted word = [1 0 1 0 1 0]
corrupted bits = [6, 3]
fixed matrix:
\[
\begin{bmatrix}
1 & 0 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
End

Start of asymmetric errors check:
input matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 1 & 0 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
check matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 0 & 0 \\
1 & 0 & 0 & 0 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 0 \\
0 & 0 & 1 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 0 & 0
\end{bmatrix}
\]
index of corrupted word = 2
code of corrupted word = [1 0 1 0 1 0]
corrupted bits = [6, 5]
fixed matrix:
\[
\begin{bmatrix}
1 & 0 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
End

Start of asymmetric errors check:
input matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 1 & 0 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 1 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
sum of rows = [3 3 4 3]
index of corrupted word = 3
code of corrupted word = [1 0 0 1 1 1]
corrupted bit = 1
fixed matrix:
\[
\begin{bmatrix}
1 & 0 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
End
Fixed!

Start of symmetric errors check:
input matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 1 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
check matrix:
\[
\begin{bmatrix}
0 & 0 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 1 \\
0 & 0 & 0 & 0 & 1 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 0
\end{bmatrix}
\]
index of corrupted word = 1
code of corrupted word = [0 0 1 1 0 1]
corrupted bits = [6, 3]
fixed matrix:
\[
\begin{bmatrix}
1 & 0 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 0 & 1 & 0 \\
1 & 0 & 0 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 1
\end{bmatrix}
\]
End
5. Conclusions

The theoretical and experimental studies carried out in the work on the evaluation of the interference resistance and corrective ability of the rank codes proposed by the authors showed that these codes are highly resistant to communication channel interference and make it possible to localize asymmetric and symmetric errors in them with the help of simple algorithms. The work theoretically proved that the Hamming distance $d_{x}$ between the codewords of the DRP code is equal to $2(m-1)-2$, which provides the possibility of localizing errors with a multiplicity of $2(m-1)-3$ and correcting errors with a multiplicity $(2(m-1)-3)/2$. The corrective ability of the proposed rank code increases with an increase in the number of code words $m$ that describe the given rank configuration of coded elements in the feature space. The conducted experimental studies of error variants in the DRP code made it possible to develop a heuristic algorithm for their localization and correction using an auxiliary verification matrix. Testing of the algorithm on a code with a bit rate of $n=6$ confirmed the adequacy of the developed algorithm for localization and correction errors of multiplicity 1. Assessment of the correctness of the algorithm's operation on codes of a higher bit rate and with errors of a higher multiplicity requires additional research.

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References


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