

An Ontological Model of Peltier Thermoelement Control based on a Fuzzy Digital Filter and a PID-controller

Maxim B. Bobyr^{1,*†}, Natalya Milostnaya² and Artem Aseev³

¹Southwest State University of Russia (SWSU), 94, 50 Let Oktyabrya St, Kursk, 305000, Russian Federation

²Southwest State University of Russia (SWSU), 94, 50 Let Oktyabrya St, Kursk, 305000, Russian Federation

³Southwest State University of Russia (SWSU), 94, 50 Let Oktyabrya St, Kursk, 305000, Russian Federation

Abstract

The ontological model of Peltier thermoelement control is presented in the article. It consists of a PID-controller, a fuzzy digital filter and an exponential moving average filter, implemented in software in the microcontroller. The ontological model calculates the voltage value, which is transmitted to the gate of the MOSFET-transistor. The field-effect transistor converts the applied voltage into a drain current signal, and this value is transmitted to the Peltier thermoelement. Voltage is removed from the Peltier thermoelement using thermistor, which is converted into a temperature value and limited from 25°C to 75°C. The technique for converting voltage to temperature is presented in the article. The temperature signal is transmitted to the input of the microcontroller. Also, a user-defined signal is fed to the input of the microcontroller, which must select the appropriate temperature value on the thermocouple. The fuzzy model, depending on the input signal, forms the coefficients of the exponential averaging filter. A limitation of the fuzzy method for calculating the coefficients used in the ontological model of thermoelement control is the use of triangular membership functions to describe the input variables. The experimental results presented in the article showed that when using a combination of a PID-controller, a fuzzy digital filter and an exponential moving average filter, the transient time during Peltier thermoelement control is reduced: overshoot reduced by 2.44%, achieved a 11.25% faster response time, and ensured 4.19% quicker stabilization.

Keywords

PID-controller, Peltier thermoelement ontology, Fuzzy logic, Fuzzy digital filter, Exponential moving average filter

1. Introduction

Systems with a PID-controller are often used in temperature control devices: a cutting tool cooling device [1], a device for regulating the temperature of a climatic chamber [2], a control system for electromechanical equipment [3], a temperature control system in a greenhouse [4], a controller for the performance and energy consumption of an industrial air conditioner [5], a control device for the air conditioning system of a car [6]. However, the PID-controller has two significant drawbacks: a large jump in the amplitude of the first harmonic of the output control signal (leading to a voltage jump that clearly accelerates the wear of the elements of the entire system) [7] and a long transient process time when the control signal goes to the specified values [8]. The third drawback of the PID-controller is the need to select the controlled coefficients K_p , K_i and K_d [9]. In one of the studies, this drawback was solved using a genetic method that allows for automatic selection of the controlled coefficients [10]. In [4], a neuro-fuzzy approach is used to solve the same drawback. In the study [2], scientists abandoned PID-control in favor of the Tsukamoto method. In this article, it is proposed to use a PID-controller modified using a combination of a fuzzy digital filter (FDF) and an exponential moving average filter (EMAF) [11] to control a Peltier thermoelement (PTE). FDF and EMAF allow to reduce the time of transient processes when controlling a PTE by reducing the jump in the amplitude of the first harmonic of the output control signal. With this approach, it is enough to set the controlled coefficients once and

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*Corresponding author.

†These authors contributed equally.

✉ maxbobyr@gmail.com (M. B. Bobyr); nat_mil@mail.ru (N. Milostnaya); asseff.artem@gmail.com (A. Aseev)

ORCID 0000-0002-5400-6817 (M. B. Bobyr); 0000-0002-3779-9165 (N. Milostnaya); 0009-0007-8271-7660 (A. Aseev)



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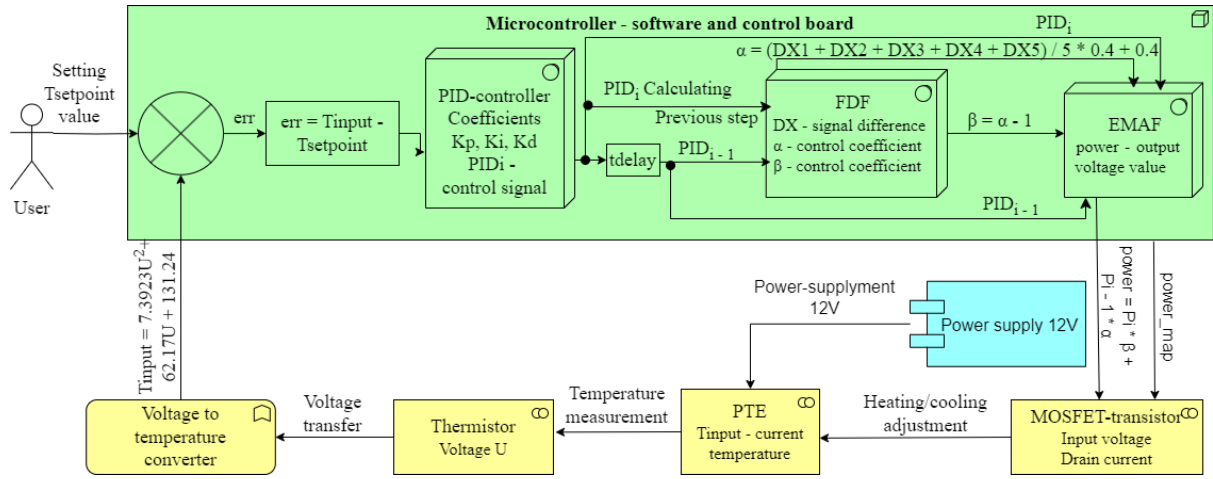


Figure 1: The ontological model of PTE control

not change them. Thus, all the main problems of the PID-controller are eliminated at once. At present, there are already modifications of the PID-controller using fuzzy logic blocks [6], [10], [12]. There are also exponential averaging modifications [9], but the combination used in this article is presented for the first time.

2. Methodological basis of the ontological model of Peltier thermoelement control

The Ontological Model of Peltier Thermoelement Control (OMPTC) allows to organize the structure of the system and describe the interaction of its components. OMPTC is represented as the following formula:

$$O_m = \left\langle \begin{matrix} 10 \\ O_c, \\ i=1 \end{matrix}, \begin{matrix} 21 \\ O_a, \\ j=1 \end{matrix}, \begin{matrix} 18 \\ O_r, \\ k=1 \end{matrix} \right\rangle \quad (1)$$

where O_c is the ontology of concepts, O_a is the ontology of attributes, O_r is the ontology of relations [13].

The graphical representation of the ontological model is shown in Fig.1.

A list of elements is used to describe the process of TE control, such as a MOSFET-transistor, PTE, thermistor, power supply and microcontroller (MC), in which the following are software implemented: comparison unit, PID-controller, FDF, EMAF; voltage converter, indicating their attributes and interrelations. The list of elements is summarized in Table 1.

The structure of the computational processes for controlling the PTE is presented as a two-level system in Fig.2. This structure allows to reduce the time of transient processes and reduce the jump in the amplitude of the first harmonic of the output control signal of the PID-controller. The first level includes the following computational processes: converting the thermistor voltage into temperature; calculating the PIDi control signal using the PID-controller; smoothing this signal using the FDF based on fuzzification of input data and the area ratio defuzzification method, forming the optimal voltage for the MOSFET-gate using the EMAF.

The second level of the system is designed to control the intensity of cooling or heating of the PTE. It includes the following computational processes: regulating the PTE power using the MOSFET-transistor, measuring the temperature on the PTE using the thermistor, transmitting the voltage by the thermistor to the voltage converter.

At the initial stage of the OMTC operation, data is received from the thermistor. For this purpose, it is necessary to calculate the temperature value T_{input} based on the dependence of the voltages at

Table 1

Specification of concepts of the OMPTC

Concepts O_c	Attributes O_a	Relationships O_r
PTE O_{c_1}	PTE type (Peltier element) O_{a_1}	Controlled by the drain current of the MOSFET transistor O_{r_1} , the temperature is measured by the thermistor O_{r_2} .
	Power O_{a_2}	
	Efficiency O_{a_3}	
Thermistor O_{c_2}	Temperature O_{a_4}	Measures the temperature of the PTE O_{r_3} . Transmits temperature data to the voltage converter O_{r_4} .
	Resistance O_{a_5}	
MC O_{c_3}	Clock frequency O_{a_6}	Controls the PID-controller and the MOSFET-transistor O_{r_5} . Connected to the zero bus O_{r_6} .
	Memory capacity O_{a_7}	
User O_{c_4}	Setpoint O_{a_8}	Sets the specified value O_{r_7} . The value goes to the comparison block O_{r_8} .
Comparison block O_{c_5}	T_{input} O_{a_9}	Receives data from the voltage converter and the set value O_{r_9} . Gives a signal about the need for regulation $O_{r_{10}}$.
	$T_{setpoint}$ $O_{a_{10}}$	
PID-controller O_{c_6}	Coefficient K_p $O_{a_{11}}$	Receives power data after a signal about the need for regulation $O_{r_{11}}$. Calculates the control signal for the FDF $O_{r_{12}}$.
	Coefficient K_i $O_{a_{12}}$	
	Coefficient K_d $O_{a_{13}}$	
	dt $O_{a_{14}}$	
	Output PID_i $O_{a_{15}}$	
FDF O_{c_7}	α $O_{a_{16}}$	Processes the control signal of the PID-controller using a smoothing algorithm $O_{r_{13}}$.
	β $O_{a_{17}}$	
EMAF O_{c_8}	Output value power $O_{a_{18}}$	From the signal coming from the FDF, it forms an output signal and transmits it to the MOSFET-transistor $O_{r_{14}}$.
MOSFET-transistor O_{c_9}	Current $O_{a_{19}}$	Receives control signal from EMAF controller $O_{r_{15}}$. Connected to zero bus $O_{r_{16}}$.
	Voltage $O_{a_{20}}$	
Power supply $O_{c_{10}}$	Voltage $O_{a_{21}}$	Connected to the 12V TE $O_{r_{17}}$, zero bus $O_{r_{18}}$.

the analog output of the thermistor, using a formula based on polynomial regression [14], which is obtained empirically:

$$T_{input} = 7.39 \times U^2 + 62.17 \times U + 131.24, \quad (2)$$

where U is the voltage at the analog input of the MC.

Thus, according to Eq.2, the MC calculates the temperature of the PTE using information coming from the thermistor, which is fixed on the surface of the PTE.

PID-controller is used to control the thermoelement, ensuring that the set temperature is maintained. For this purpose, a controlled signal is calculated, the task of which is to reduce the difference between the temperature set by the user $T_{setpoint}$ and the actual T_{input} received from the thermistor in the MC:

$$err = T_{setpoint} - T_{input} \rightarrow min. \quad (3)$$

The coefficients proportional K_p , integrating K_i , differentiating K_d , integration step dt have specific values and do not need to be calculated. Thus, the following signal is generated at the output of the PID-controller [15]:

$$PID_i = err \times K_p + err \times dt \times K_i + \frac{(err_i - err_{i-1}) \times K_d}{dt}. \quad (4)$$

From the control signal of the PID-controller PID_i , a delay signal PID_{i-1} is formed, determined after a specified time interval t_{delay} . Both signals are transmitted to the FDF for further smoothing using a fuzzy rule base.

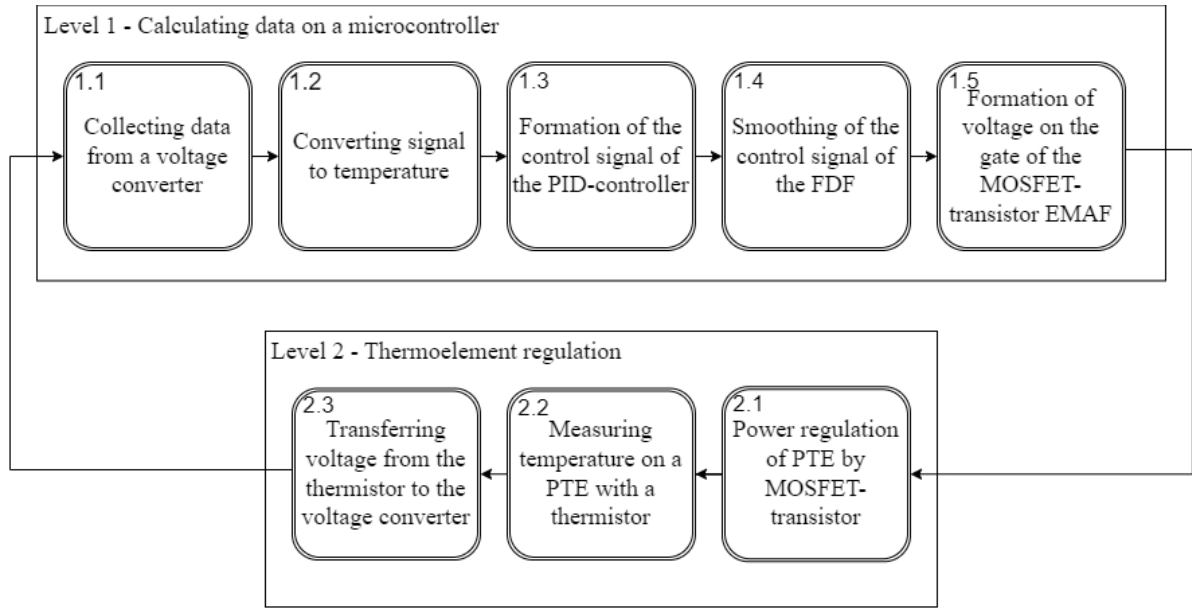


Figure 2: The structure of computational processes in a two-level PTE control system

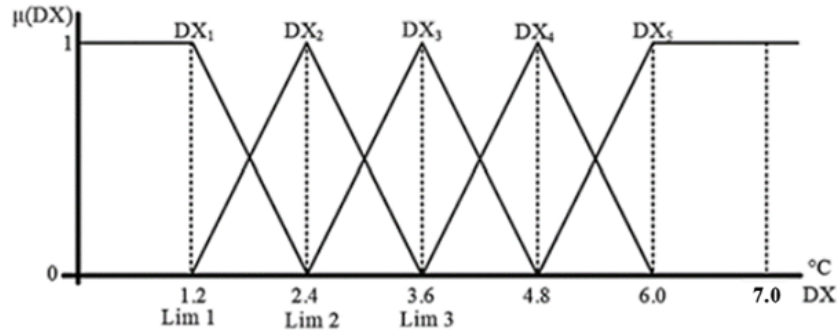


Figure 3: Input membership functions, where the labels Lim_1 , Lim_2 , Lim_3 are designated for the second membership function DX_2

After the PID-controller generates the control signal, it is necessary to smooth it to eliminate sharp jumps and reduce the load on the MOSFET-transistor. For this purpose, a FDF is used, which eliminates high-frequency interference in the signal. The EMAF signal smoothing formula is formulated as follows:

$$DX = PID_i - PID_{i-1}. \quad (5)$$

where PID_i is the current signal of the PID-controller, PID_{i-1} is the delay signal determined after a specified time interval dt .

Transform the variable DX (see Eq.5) into a linguistic variable with terms $DX = DX_1, DX_2, DX_3, DX_4, DX_5$. The core of the input linguistic variable is the range of values from 0.0 to 7.0. The graph of the input membership function is shown in Fig.3.

The output linguistic variable is the control coefficient α , consisting of five terms: M_1, M_2, M_3, M_4, M_5 , which is set by a proportional value in the range from 40% to 80% [0.4; 0.8] of its maximum value [17]. The bases of the input membership functions (see Eqs.6-8) and fuzzy rules (see Eqs.9-13) are presented below:

$$\mu(DX)_1 = \begin{cases} 1, & \text{if } DX > 0 \text{ and } DX < Lim_1 \\ \frac{Lim_2 - DX}{Lim_2 - Lim_1}, & \text{if } DX > Lim_1 \text{ and } DX < Lim_2 \\ 0, & \text{else;} \end{cases} \quad (6)$$

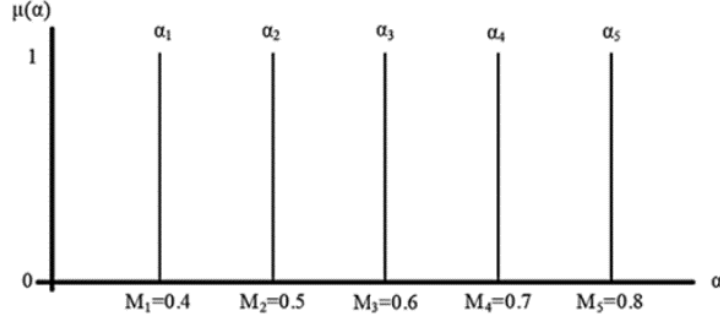


Figure 4: Output membership functions

$$\mu(DX)_{2,3,4} = \begin{cases} \frac{DX-Lim_1}{Lim_2-Lim_1}, & \text{if } DX > Lim_1 \text{ and } DX < Lim_2 \\ \frac{Lim_3-DX}{Lim_3-Lim_2}, & \text{if } DX > Lim_2 \text{ and } DX < Lim_3 \\ 0, & \text{else;} \end{cases} \quad (7)$$

$$\mu(DX)_5 = \begin{cases} \frac{DX-Lim_1}{Lim_2-Lim_1}, & \text{if } DX > Lim_1 \text{ and } DX < Lim_2 \\ 1, & \text{if } DX > Lim_2 \text{ and } DX < Lim_3 \\ 0, & \text{else;} \end{cases} \quad (8)$$

$$IF \ DX_1 \ THEN \ M_1 \quad (9)$$

$$IF \ DX_2 \ THEN \ M_2 \quad (10)$$

$$IF \ DX_3 \ THEN \ M_3 \quad (11)$$

$$IF \ DX_4 \ THEN \ M_4 \quad (12)$$

$$IF \ DX_5 \ THEN \ M_5 \quad (13)$$

The graph of the output membership function is shown in Figure 4. The control coefficient α is calculated using the following formula:

$$\alpha = \frac{\sum_{i=1}^5 DX_i - M_i}{\sum_{i=1}^5 DX_i} \quad (14)$$

Calculating the β coefficient:

$$\beta = 1 - \alpha. \quad (15)$$

The coefficient β is necessary for the final calculation of the output voltage value from EMAF U_g . The U_g value is calculated according to the following form:

$$U_g = PID_{i-1} \times \alpha + PID_i \times \beta. \quad (16)$$

After calculating the voltage U_g , it is converted into a range suitable for the eight-bit DAC at the output of the MC (Arduino) [16]:

$$power_{map} = U_g \times 100/255. \quad (17)$$

The output voltage U_g after processing by the EMAF is transferred to the drain of the MOSFET-transistor, which is used to switch the power of the PTE [17].

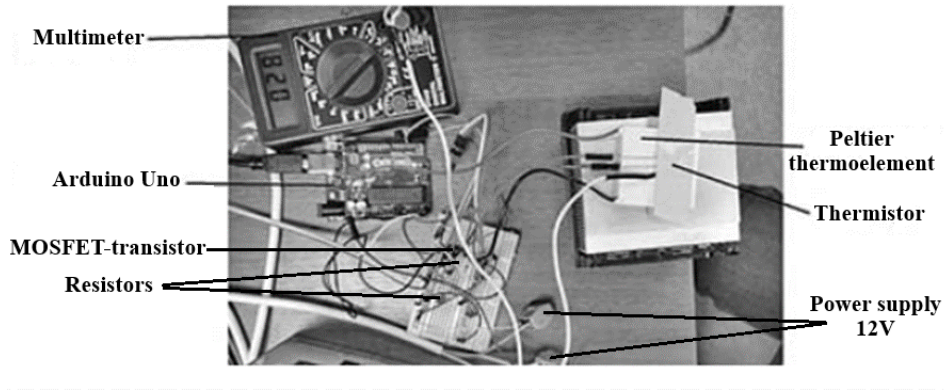


Figure 5: The experimental setup OMPTC with FDF

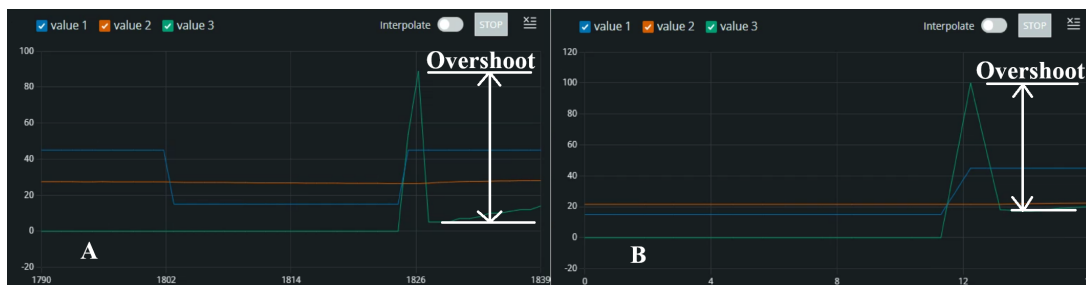


Figure 6: OMPTC reaction when user set $T_{setpoint}$: A is default PID-regulator, B is PID-regulator with FDF and EMAF

3. Experimental research

The characteristics of the OMPTC were determined by conducting experimental studies. The experimental setup of the control system with software-implemented FDF, EMAF and PID-regulator is shown in Fig.5.

The objective of the experiment was to compare the performance of the OMPTC system using the default PID-regulator against the enhanced PID-regulator with FDF and EMAF. During the experiment, the user set a target temperature of $T_{setpoint} = 45^{\circ}\text{C}$, represented by the value1 signal (blue). This signal remained constant over time, indicating that the system was maintaining the desired temperature.

Signal value2 (orange) represents the current measured temperature T_{input} from the PTE obtained via a thermistor. When value1 changes, the control system adjusts the PTE temperature to approach the target value, demonstrating effective regulation.

Signal value3 (green): This is the control signal representing the voltage $power_{map}$ applied from MC (Arduino) to the MOSFET-transistor. The MOSFET-transistor regulates the power supplied to the PTE. The sharp spikes and subsequent drops in this signal indicate the process of power regulation to minimize the deviation from the target temperature. The behavior of the default PID-regulator and the enhanced PID-regulator (with FDF and EMAF) is shown in Figs.6-8:

The results of the experiment are summarized in Table 2.

Table 2
OMPTC default PID-regulator and FDF EMAF PID-regulator comparison

Characteristics	default PID-regulator	FDF and EMAF PID-regulator
Overshoot reduction	82	80
Response time	1:20	1:11
Stabilization time	5:34	5:20

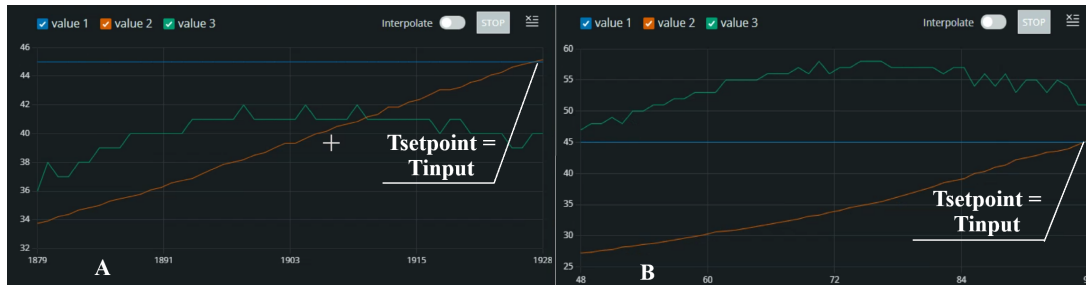


Figure 7: T_{input} equal $T_{setpoint}$ moment: A is default PID-regulator, B is PID-regulator with FDF and EMAF

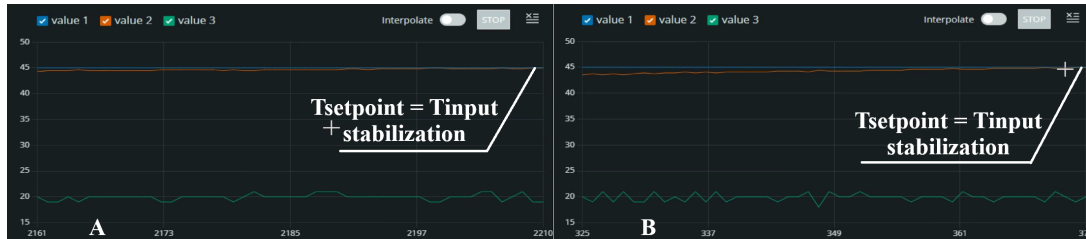


Figure 8: Convergence of $T_{setpoint}$ and T_{input} at the end of PID-adjustment: A is default PID-regulator, B is PID-regulator with FDF and EMAF

4. Conclusion

The experiment aimed to compare the performance of the OMPTC system with a default PID-regulator against the OMPTC system enhanced with FDF and EMAF in terms of temperature control and stabilization. Based on the results, the FDF and EMAF PID-regulator demonstrated better performance across all key indicators: it reduces overshoot by 2.44%, achieves a 11.25% faster response time, and ensures 4.19% quicker stabilization.

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Declaration on Generative AI

The author(s) have not employed any Generative AI tools.

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