# Satisfiability Modulo Theories 22<sup>nd</sup> International Workshop SMT 2025

and

Pragmatics of SAT 16<sup>th</sup> International Workshop PoS 2025

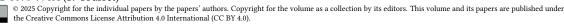
# **Proceedings**

Glasgow, Scotland, UK Affiliated with SAT, CP and SoCS 2025 August 10–11, 2025

Jochen Hoenicke<sup>1,†</sup>, Aina Niemetz<sup>2,‡</sup>, Mikoláš Janota<sup>3,‡</sup> and Sophie Tourret<sup>4,†</sup>

Satisfiability Modulo Theories (SMT) 2025 and Pragmatics of SAT (PoS) 2025, August 10-11, 2025, Glasgow, UK

<sup>© 0000-0002-6314-1041 (</sup>J. Hoenicke); 0000-0003-2600-5283 (A. Niemetz); 0000-0003-3487-784X (M. Janota); 0000-0002-6070-796X (S. Tourret)



<sup>&</sup>lt;sup>1</sup>Certora GmbH, Schleißheimer Str. 23, 80333 München, Germany

<sup>&</sup>lt;sup>2</sup>Stanford University, Department of Computer Science, Stanford, CA 94305, USA

<sup>&</sup>lt;sup>3</sup>Czech Technical University in Prague, Jugoslávských partyzánů 1580/3, 160 00 Prague 6, Czechia

<sup>&</sup>lt;sup>4</sup>Inria centre at Université de Lorraine, France

<sup>&</sup>lt;sup>†</sup>Satisfiability Modulo Theories (SMT) 2025

<sup>&</sup>lt;sup>‡</sup>Pragmatics of SAT (PoS) 2025

应 jochen@certora.com (J. Hoenicke); niemetz@cs.stanford.edu (A. Niemetz); mikolas.janota@gmail.com (M. Janota); stourret@loria.fr (S. Tourret)

thttps://jhoenicke.de (J. Hoenicke); https://cs.stanford.edu/~niemetz (A. Niemetz); https://people.ciirc.cvut.cz/~janotmik (M. Janota); https://members.loria.fr/STourret/ (S. Tourret)

# **Preface**

The  $23^{\rm rd}$  International Workshop on Satisfiability Modulo Theory (SMT 2025) and the  $16^{\rm th}$  International Workshop on Pragmatics of SAT (PoS 2025) were held in Glasgow, UK, on August  $10^{\rm th}$  and  $11^{\rm th}$  in association with the  $28^{\rm th}$  International Conference on Theory and Application of Satisfiability Testing (SAT), the  $31^{\rm st}$  International Conference on Principles and Practice of Constraint Programming (CP), and the  $18^{\rm th}$  International Symposium on Combinatorial Search (SoCS).

#### **SMT 2025**

The SMT workshop is an annual event dedicated to Satisfiability Modulo Theories (SMT). Determining the satisfiability of first-order formulas modulo background theories, known as the Satisfiability Modulo Theories problem, has proved to be an enabling technology for verification, synthesis, test generation, compiler optimization, scheduling, and other areas. The success of SMT techniques depends on the development of both domain-specific decision procedures for each background theory (e.g., linear arithmetic, the theory of arrays, or the theory of bit-vectors) and combination methods that allow one to obtain more versatile SMT tools, usually leveraging Boolean satisfiability (SAT) solvers. These ingredients together make SMT techniques well-suited for use in larger automated reasoning and verification efforts. The aim of the workshop is to bring together researchers and users of SMT tools and techniques. Relevant topics include but are not limited to:

- Decision procedures and theories of interest
- Combinations of decision procedures
- Novel implementation techniques
- · Benchmarks and evaluation methodologies
- · Applications and case studies
- Theoretical results

SMT 2025 featured invited talks by Jan Strejček from Masaryk University and Katalin Fazekas from TU Wien. There were 19 presentations of peer-reviewed papers. The workshop received 24 submissions, out of which 19 were accepted. Each submission was reviewed by three program committee members. Of the 19 accepted submissions, 13 are published in this volume: three as original papers, and ten as extended abstracts. The remaining six were submitted to the workshop for presentation only.

We would like to thank the program committee, the subreviewers, the authors, the invited speakers, the SMT-COMP organizers, workshop participants and the SMT Steering Committee for their contribution to the workshop. We would further like to thank the CP/SAT/SoCS organizers for hosting the workshop, EasyChair for the availability of their conference system, and CEUR for their help to publish these proceedings.

SMT 2025 was sponsored by Certora and the Ethereum Foundation. We are grateful for their generosity in supporting the workshop.

Jochen Hoenicke and Sophie Tourret SMT 2025 Chairs

#### PoS 2025

The Pragmatics of SAT (PoS) workshop is an annual event dedicated to providing a forum for discussion and presentation of the design and application of SAT solver and related solver technologies. This includes (but is not restricted to) Satisfiability Modulo Theories (SMT), Answer Set Programming (ASP), Constraint Programming (CP).

The success of solver technologies for declarative languages such as Boolean satisfiability (SAT) over the last two decades is mainly due to both the availability of efficient solver implementations and the growing number of problems that can efficiently be solved through the declarative approach. Designing efficient solvers requires both the understanding of fundamental underlying algorithms and expertise in how to implement such algorithms as efficient and robust solvers.

The PoS workshop series is organized together with the International Conferences on Theory and Applications of Satisfiability Testing (SAT). The year 2025 constituted the 16<sup>th</sup> edition of the workshop.

PoS 2025 received a total of 11 submissions, out of which 10 were accepted. Each submission was reviewed by three program committee members. Out of the 10 accepted submissions, 4 are published in this volume as original papers and the remaining 6 were submitted for presentation only.

During the workshop, we honored the memory of Allen Van Gelder, one of the founding members, with a short tribute following his passing on April 25, 2025.

We would like to thank the program committee, the authors, the workshop participants and the PoS Steering Committee for their contributions to the workshop. We would further like to thank the CP/SAT/SoCS organizers for hosting the workshop, EasyChair for the availability of their conference system, and CEUR for their help to publish these proceedings.

Mikoláš Janota and Aina Niemetz PoS 2025 PC Chairs

#### **Peer Reviewers**

#### **SMT 2025**

#### **Program Chairs**

*Jochen Hoenicke*, Certora, Germany *Sophie Tourret*, Inria, France

### **Program Committee**

Haniel Barbosa, Universidade Federal de Minas Gerais, Brazil Nikolaj Bjørner, Microsoft, US Maria Paola Bonacina, Università degli Studi di Verona, Italy Guillaume Bury, OCamlPro, France Bruno Dutertre, Amazon Web Services, US Katalin Fazekas, TU Wien, Austria Pascal Fontaine, Université de Liège, Belgium Florian Frohn, RWTH Aachen University, Germany Alessandro Gianola, Lisbon University, Portugal Stéphane Graham-Lengrand, SRI International, US Alberto Griggio, Fondazione Bruno Kessler, Italy Martin Jonáš, Masaryk University, Czechia Alexander Nadel, Technion & NVIDIA, Israel Aina Niemetz, Stanford University, US Mathias Preiner, Stanford University, US Philipp Rümmer, University of Regensburg, Germany Hans-Jörg Schurr, University of Iowa, US Natasha Sharygina, University of Lugano, Switzerland Cesare Tinelli, The University of Iowa, US Yoni Zohar, Bar-Ilan University, Israel

#### **Subreviewers**

Konstantin Britikow, University of Lugano, Switzerland Clemens Eisenhofer, TU Wien, Austria Fajar Haifani, Fondazione Bruno Kessler, Italy Faezeh Labbaf, University of Lugano, Switzerland Jasper Nalbach, RWTH Aachen, Germany David Perera, Universidade Federal de Minas Gerais, Brazil Andrew Reynolds, University of Iowa, US Kartik Sabharwal, University of Iowa, US Roberto Sebastiani, Università di Trento, Italy Yogev Shalmon, Intel, Israel Sarah Winkler, University of Bozen-Bolzano, Italy

#### **PoS 2025**

# **Program Chairs**

Mikoláš Janota, Czech Technical University in Prague, Czechia Aina Niemetz, Stanford University, US

#### **Program Committee**

Gilles Audemard, Université d'Artois, France
Armin Biere, University of Freiburg, Germany
Jip Dekker, Monash University, Australia
Leroy Chew, TU Wien, Austria
Katalin Fazekas, TU Wien, Austria
Marijn Heule, Carnegie Mellon University, US
Alexey Ignatiev, Monash University, Australia
Vasco Manquinho, INESC-ID / IST, Universidade de Lisboa, Portugal
Alexander Nadel, Technion & NVIDIA, Israel
Nina Narodytska, VMWare, US
Mathias Preiner, Stanford University, US
Mate Soos, Ethereum Foundation, Germany, and National University of Singapore
Peter Stuckey, Monash University, Australia

# **Contents**

# **SMT**

Invited Talks Abstracts	
Deciding Satisfiability of Quantified Bitvector Formulae with BDDs	1
SAT Reasoning in CDCL(T) Solvers	2
Original Papers	
Evaluating Binary Polynomials using Subpolynomials	3
An Optimization Modulo Theories-Based Approach to Cumulative Scheduling with Delays .  Antton Kasslin, Jeremias Berg	16
A Proposal for an OMT Extension to SMT-LIB	29
Extended Abstracts	
Quantifier Instantiations: To Mimic or To Revolt	45
From MBQI to Enumerative Instantiation and Back	52
A Few Exercises on the Complexity of Congruence Closure with Cardinality Constraints Ellen Dasnois, Pascal Fontaine	59
Constraint Propagation for Bit-Vectors in Alt-Ergo	65
Syntax-Guided Synthesis with Counterexample-Guided E-graphs: A Work-in-Progress Report Guy Frankel, Rudi Schneider, Michel Steuwer, Elizabeth Polgreen	: 77
On Writing SMT-LIB Scripts: Metrics and a New Dataset	91
Comparative Analysis of SMT Solvers for Differential Cryptanalysis of SHA-2	103
Visualization of Execution Traces in Colibri 2 SMT Solver	126
A Conjecture Regarding SMT Instability	136
Instability Track for SMT-COMP	148
PoS	
Revisiting Clause Vivification	153
SAT-Web: A Web-Based Educational SAT Visualisation Tool	168
James Maagwick, Martin Mariusz Lester Improving Watched Pseudo-Boolean Propagation with Significant Literals	177
Incremental Inprocessing Rules beyond Resolution	190