







Workshop Proceedings

ACESMB 2009

Second International Workshop on Model Based Architecting and Construction of Embedded Systems

October 6th, 2009, Denver, Colorado, USA

Organized in conjunction with MoDELS'09

12th International Conference on Model Driven Engineering Languages and Systems

Edited by:

Stefan Van Baelen (K.U.Leuven - DistriNet, Belgium)
Thomas Weigert (Missouri University of Science and Technology, USA)
Ileana Ober (University of Toulouse - IRIT, France)
Huascar Espinoza (CEA - LIST, France)







Table of Contents

Table of Contents	3
Foreword	5
Acknowledgments	7
Invited Talk - Semantics preservation issues in the design and optimization of SW architect automotive systems	ures for
Marco Di Natale (Scuola Superiore Sant'Anna, Italy)	9
SOPHIA: a Modeling Language for Model-Based Safety Engineering	
Daniela Cancila, François Terrier (CEA LIST), Fabien Belmonte (ALSTOM), Hubert Dubois, H Espinoza, Sébastien Gérard and Arnaud Cuccuru (CEA LIST)	
PaNeCS: A Modeling Language for Passivity-based Design of Networked Control Systems	
Emeka Eyisi, Joseph Porter, Joe Hall, Nicholas Kottenstette, Xenofon Koutsoukos and Janos Sztipanovits(Vanderbilt University, USA)	
Formal Design Models for Distributed Embedded Control Systems	
Christo Angelov, Krzysztof Sierszecki and Yu Guo (University of Southern Denmark, Denma	ırk) 43
Improving Timing Analysis for Matlab Simulink/Stateflow	
Lili Tan, Björn Wachter, Philipp Lucas and Reinhard Wilhelm (Universität des Saarlandes, G	• •
Prototyping of Distributed Embedded Systems Using AADL	
Mohamed Yassin Chkouri and Marius Bozga (VERIMAG, France)	65
Towards Intelligent Tool-Support for AADL Based Modeling of Embedded Systems	
Dries Langsweirdt, Yves Vandewoude and Yolande Berbers (K.U.Leuven, Belgium)	81
Model-Based Codesign of Critical Embedded Systems	
Marco Bozzano, Alessandro Cimatti (Fondazione Bruno Kessler, Italy), Joost-Pieter Katoen, Nguyen, Thomas Noll (RWTH Aachen University, Germany) and Marco Roveri (Fondazione Kessler, Italy)	e Bruno
Design Complexity Management in Embedded System Design	
Johan Ersfolk, Johan Lilius (Åbo Akademi University, Finland), Jari Muurinen, Ari Salomäki Devices, Finland), Niklas Fors and Johnny Nylund (Åbo Akademi University, Finland)	•
Using Higher-order Transformations to Derive Variability Mechanism for Embedded Syster	ns
Goetz Botterweck (Lero, Ireland), Andreas Polzer and Stefan Kowalewski (RWTH Aachen University, Germany)	107
Model-Based Extension of AUTOSAR for Architectural Online Reconfiguration	
Basil Becker, Holger Giese, Stefan Neumann, Martin Schenck and Arian Treffer (University Potsdam, Germany)	of 123

Foreword

The development of embedded systems with real-time and other critical constraints raises distinctive problems. In particular, development teams have to make very specific architectural choices and handle key non-functional constraints related to, for example, real-time deadlines and to platform parameters like energy consumption or memory footprint. In this context, the last few years have seen an increased interest in using model-based engineering (MBE) techniques. MBE techniques are interesting and promising for the following reasons: They allow to capture dedicated architectural and non-functional information in precise (and even formal) domain-specific models, and they support a layered construction of systems, in which the (platform independent) functional aspects are kept separate from architectural and non-functional (platform specific) aspects, where the final system is obtained by combining these aspects later using model transformations.

The objective of this workshop is to bring together researchers and practitioners interested in model-based software engineering for real-time embedded systems. We are seeking contributions relating to this subject at different levels, from modeling languages and semantics to concrete application experiments, from model analysis techniques to model-based implementation and deployment. Given the criticality of the application domain, we particularly focus on model-based approaches yielding efficient and provably correct designs. Concerning models and languages, we welcome contributions presenting novel modeling approaches as well as contributions evaluating existing ones. The workshop targets in particular:

- Architecture description languages (ADLs). Architecture models are crucial elements
 in system and software development, as they capture the earliest decisions which
 have a huge impact on the realization of the (non-functional) requirements, the
 remaining development of the system or software, and its deployment. We are
 particularly interested in examining:
 - o Position of ADLs in an MDE approach;
 - Relations between architecture models and other types of models used during requirement engineering (e.g., SysML, EAST-ADL, AADL), design (e.g., UML), etc.;
 - Techniques for deriving architecture models from requirements, and deriving high-level design models from architecture models;
 - Verification and early validation using architecture models.

- Domain specific design and implementation languages. To achieve the high confidence levels required for critical embedded systems through analytical methods, in practice languages with particularly well-behaved semantics are often used, such as synchronous languages and models (Lustre/SCADE, Signal/Polychrony, Esterel), super-synchronous models (TTA, Giotto), scheduling-friendly models (HRT-UML, Ada Ravenscar), or the like. We are interested in examining the model-oriented counterparts of such languages, together with the related analysis and development methods.
- Languages for capturing non-functional constraints (MARTE, AADL, OMEGA, etc.)
- Component languages and system description languages (SysML, MARTE, EAST-ADL, AADL, BIP, FRACTAL, Ptolemy, etc.).

We accepted 10 papers for the workshop from 8 different countries: 7 full papers and 3 short papers. We hope that the contributions for the workshop and the discussions during the workshop will help to contribute and provide interesting new insights in Model Based Architecting and Construction of Embedded Systems.

The ACES^{MB} 2009 organizing committee,

Stefan Van Baelen, Thomas Weigert, Ileana Ober, Huascar Espinoza.

The ACES^{MB} 2009 steering committee,

Mamoun Filali, Sébastien Gérard, Susanne Graf, Iulian Ober.

September 2009.

Acknowledgments

The Organizing Committee of ACES^{MB} 2009 would like to thank the workshop Program Committee for their helpful reviews.

Nicolas Belloir (LIUPPA, France)

Jean-Michel Bruel (IRIT, France)

Agusti Canals (CS, France)

Arnaud Cuccuru (CEA-LIST, France)

Huascar Espinoza (CEA LIST, France)

Jean-Marie Farines (UFSC, Brasil)

Peter Feiler (SEI, USA)

Mamoun Filali (IRIT, France)

Robert France (CSU, USA)

Pierre Gaufillet (Airbus, France)

Sébastien Gérard (CEA LIST, France)

Susanne Graf (VERIMAG, France)

Bruce Lewis (US Army, USA)

Ileana Ober (IRIT, France)

Iulian Ober (IRIT, France)

Isabelle Perseil (Telecom ParisTech, France)

Dorina Petriu (Carleton University, Canada)

Bernhard Rumpe (RWTH Aachen, Germany)

Douglas C. Schmidt (Vanderbilt University, USA)

Bran Selic (Malina Software, Canada)

Jean-Bernard Stefani (INRIA, France)

Richard Taylor (UCI, USA)

Martin Törngren (KTH, Sweden)

Stefan Van Baelen (K.U.Leuven DistriNet, Belgium)

Tullio Vardanega (University of Padua, Italy)

Eugenio Villar (Universidad de Cantabria, Spain)

Thomas Weigert (Missouri S&T, USA)

Tim Weilkiens (OOSE, Germany)

Sergio Yovine (VERIMAG, France)

This workshop is organised as an event in the context of

- The IST-004527 ARTIST2 Network of Excellence on Embedded Systems Design
- The research project EUREKA-ITEA SPICES (Support of Predictable Integration of mission Critical Embedded Systems)